

Compal Confidential

Model Name :  
File Name : LA-8671P

Compal Confidential  
TN-Note Schematic Document

2012-10-04

REV:1A\_1004A

|   |  |                    |  |                 |  |                          |  |            |                            |       |
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|   |  |                    |  |                 |  |                          |  | Size       | Document Number            | Rev   |
|   |  |                    |  |                 |  |                          |  | Custom     | LA-8671P_SDV               | 1A    |
|   |  |                    |  |                 |  |                          |  | Date:      | Thursday, October 04, 2012 | Sheet |

**Model Name : TN-Note**  
**File Name : Block Diagram**



|  |                    |                 |                          |                       |
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|  |                    |                 |                          | LA-6872               |
|  |                    |                 |                          | TN-Note Block Diagram |

Voltage Rails

| Power plane          |    |                  |       |                 |   |
|----------------------|----|------------------|-------|-----------------|---|
| State                | +B | +5VALW<br>+3VALW | +1.5V | +3VM<br>+1.05VM | +5VS<br>+3VS<br>+1.8VS<br>+1.5VS<br>+1.05VS<br>+VCC_GFXCORE_AXG<br>+CPU_CORE<br>+VCCSA<br>+0.75VS |
| S0                   | ○  | ○                | ○     | ○               | ○   |
| S3                   | ○  | ○                | ○     | ○               | ✗   |
| M3                   | ○  | ○                | ○     | ○               | ✗   |
| S4/S5 - AC           | ○  | ○                | ✗     | ✗               | ✗   |
| S4/S5 - BATT ONLY    | ○  | ✗                | ✗     | ✗               | ✗   |
| S4/S5 - NO AC & BATT | ✗  | ✗                | ✗     | ✗               | ✗   |

BOM Structure

|                         | SDV | FVT | SIT | SVT | SOVP |
|-------------------------|-----|-----|-----|-----|------|
| @ : No Stuff            |     |     |     |     |      |
| ME@ : ME components     |     |     |     |     |      |
| CPUx@ : CPU SKU         | V   | V   | V   | V   | V    |
| DDRx@ : RAM SKU         | V   | V   | V   | V   | V    |
| SBA@ : SBA              | V   | V   | V   | V   | V    |
| NOSBA@ : NO SBA         |     |     |     |     |      |
| ID4@ : Intel Deep S4    | V   |     |     |     |      |
| AOAC@ : AOAC            | V   | V   | V   | V   | V    |
| TPM@ : TPM              |     | V   | V   | V   | V    |
| Short@ : 0ohm short pad |     |     |     |     |      |

| STATE \ SIGNAL       | SLP_S3# | SLP_S4# | SLP_S5# | +VALW | +V  | +VS |
|----------------------|---------|---------|---------|-------|-----|-----|
| Full ON              | H       | H       | H       | ON    | ON  | ON  |
| S3 (Suspend to RAM)  | L       | H       | H       | ON    | ON  | OFF |
| S4 (Suspend to Disk) | L       | L       | H       | ON    | OFF | OFF |
| S5 (Soft OFF)        | L       | L       | L       | ON    | OFF | OFF |

BOARD ID Table

| Board ID | PCB Revision |
|----------|--------------|
| 0        | 0.1          |
| 1        | 0.2          |
| 2        | 0.3          |
| 3        | 0.4          |
| 4        | 0.5          |
| 5        |              |
| 6        |              |
| 7        |              |

EC SM Bus1 address

| Device        | Address    | HEX | Device                  | Address     | HEX |
|---------------|------------|-----|-------------------------|-------------|-----|
| Smart Battery | 0001-011xb | 16H | Thermal sensor          | 1001-101xb  | 9AH |
| Charger       | 0001-0010b | 12H | PCH (SML1DATA / GPIO75) | 1001-0110 b | 96H |

EC SM Bus2 address

SM Bus Controller

| Device       | Address    | HEX  |
|--------------|------------|------|
| Security Rom | 1010-100xb | A8 H |
| DDR DIMMO    | 1010-000xb | A0 H |

SM Bus Controller 0

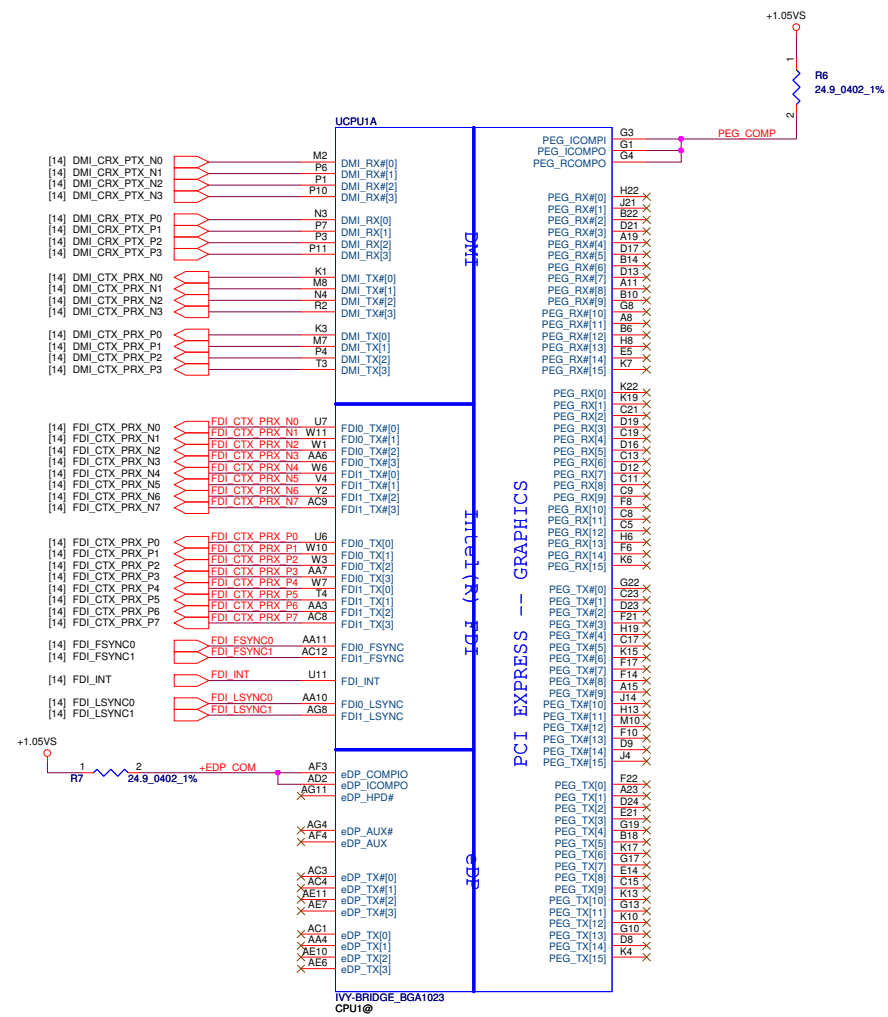
| Device | Address | HEX    |
|--------|---------|--------|
| No-use | No-use  | No-use |

Sensor HUB

| Device    | Address    | HEX |
|-----------|------------|-----|
| ALS       | 0100-100xb | 48h |
| APS       | 0011-001xb | 32h |
| Gyroscope | 0110-101xb | 6Ah |
| e-Compass | 0011-110xb | 3Ch |

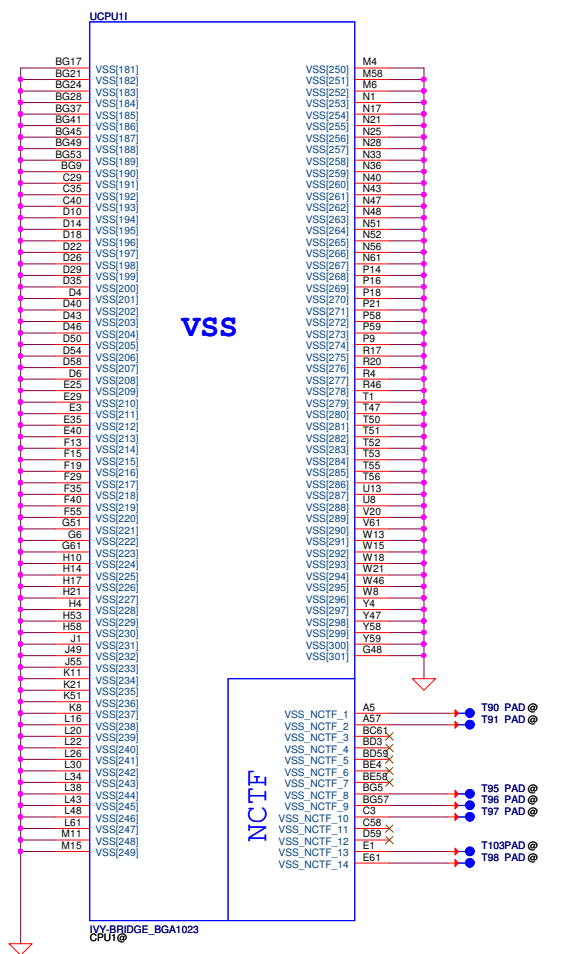
| USB3.0 | USB2.0 | NOTE             |
|--------|--------|------------------|
| 1      | 0      |                  |
| 2      | 1 *    | USB3.0/2.0 Conn  |
| 3      | 2      | USB3.0/2.0 Conn  |
| 4      | 3      |                  |
|        | 4      | Sensor Hub       |
|        | 5      | USB Camera       |
|        | 6      | ✗                |
|        | 7      | ✗                |
|        | 8      | Touch Panel      |
|        | 9 *    | WWAN             |
|        | 10     | WLAN             |
|        | 11     | Finger Printer** |
|        | 12     |                  |
|        | 13     | Bluetooth**      |

\* Debug Port  
\*\* Not Use



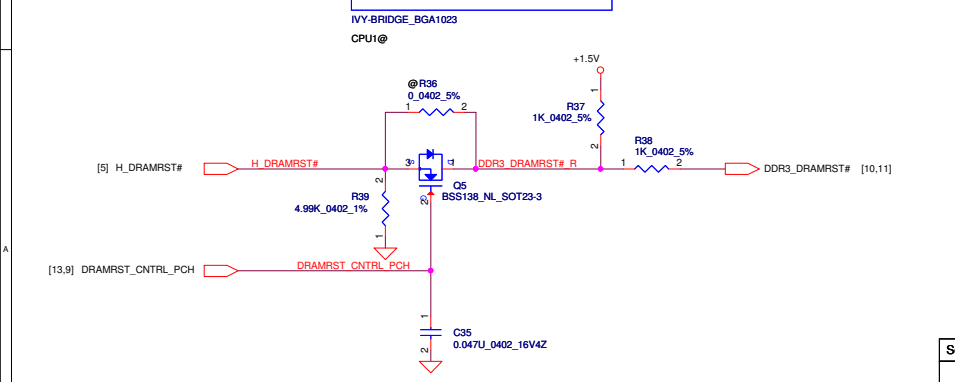
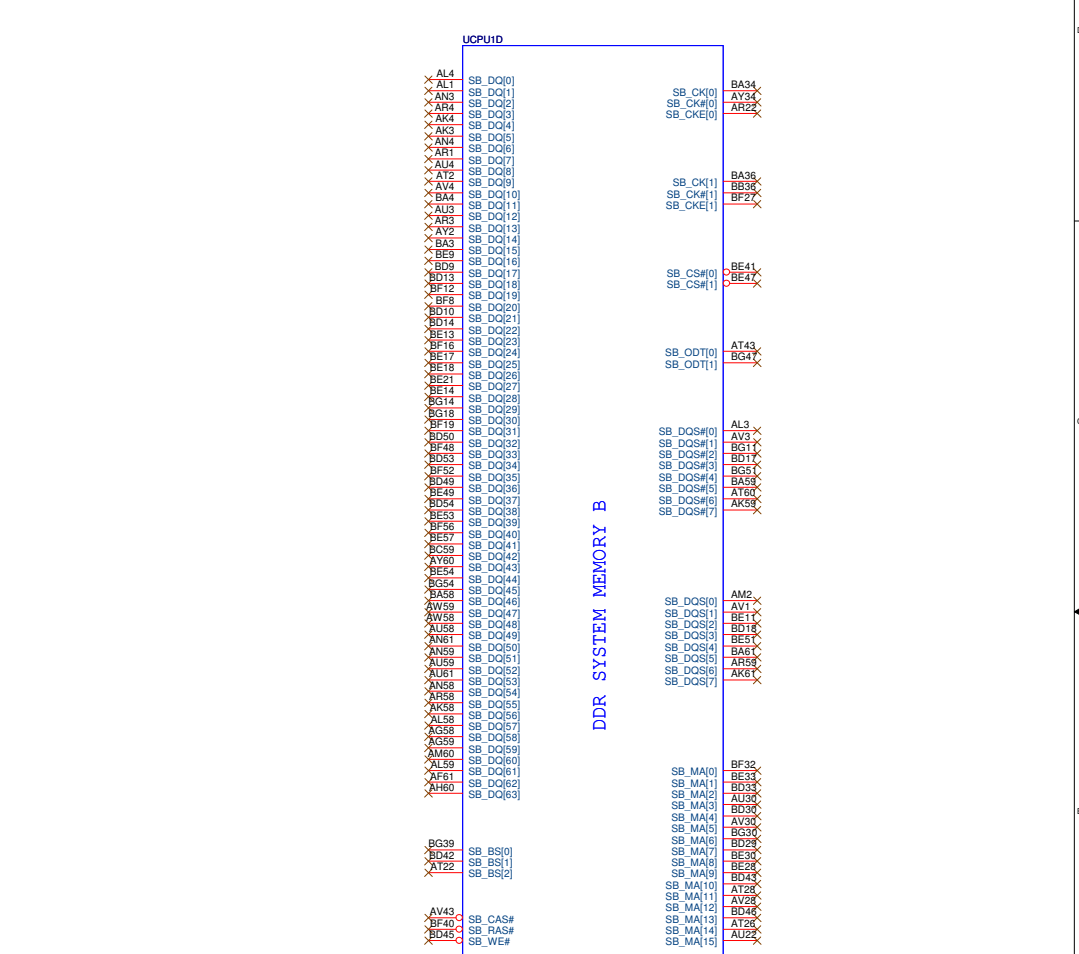
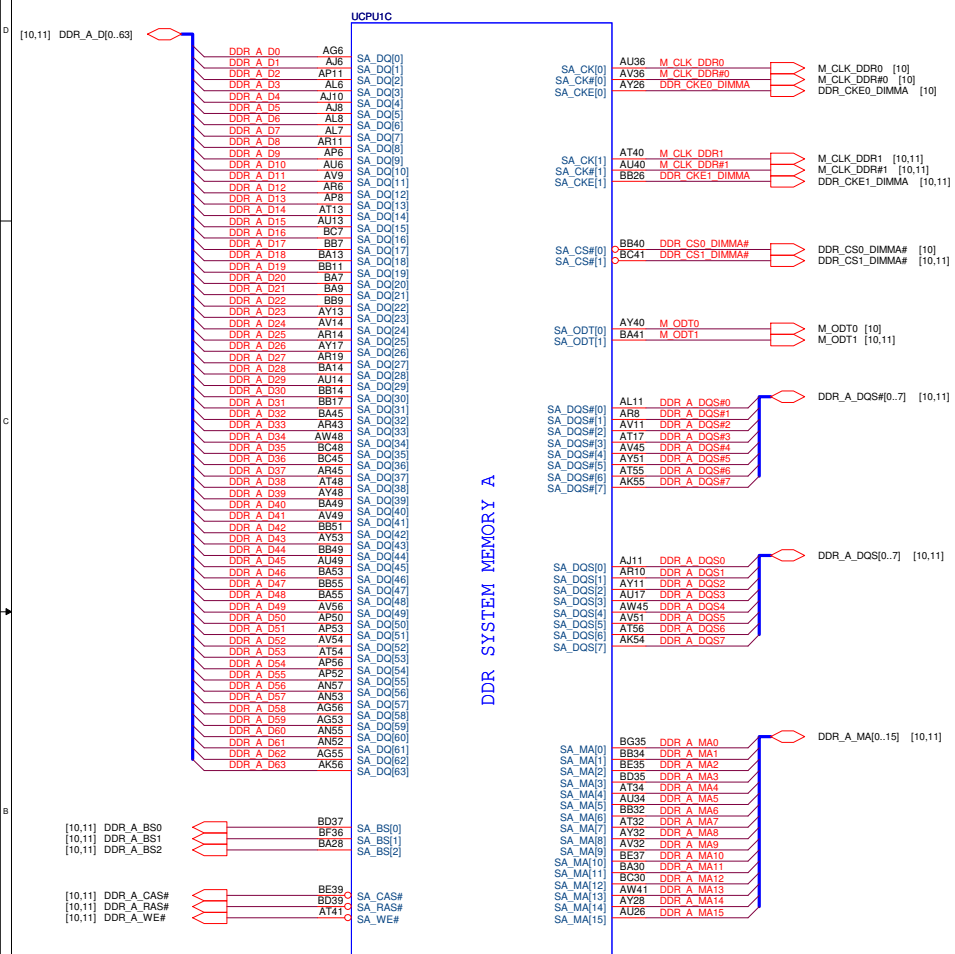
PEG\_ICOMPI and RCOMP signals should be shorted and routed with - max length = 500 mils - typical impedance = 43 mohms  
PEG\_ICOMPO signals should be routed with - max length = 500 mils - typical impedance = 14.5 mohms

|      |  |
|------|--|
| CFG2 | ★ 1: Normal Operation; Lane # definition matches socket pin map definition<br>0: Lane Reversed |
|------|--|



|   |            |                    |            |       |                            |                          |         |
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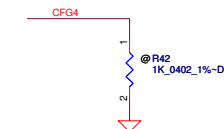


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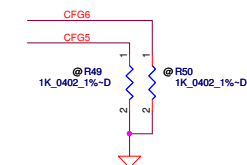
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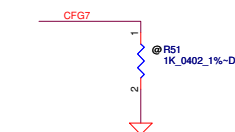
|      |   |
|------|---|
| CFG2 | *1:(Default) Normal Operation; Lane #<br>definition matches socket pin map<br>definition<br>0:Lane Reversed |
|------|---|



|      |   |
|------|---|
| CFG4 | <p>* 1 : Disabled; No Physical Display Port attached to Embedded Display Port</p> <p>0 : Enabled; An external Display Port device is connected to the Embedded Display Port</p> |
|------|---|



|          |   |
|----------|---|
| CFG[6:5] | *11: (Default) x16 - Device 1 functions 1 and 2 disabled<br>10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled<br>01: Reserved - (Device 1 function 1 disabled ; function 2 enabled)<br>00: x8,x4,x4 - Device 1 functions 1 and 2 enabled |
|----------|---|



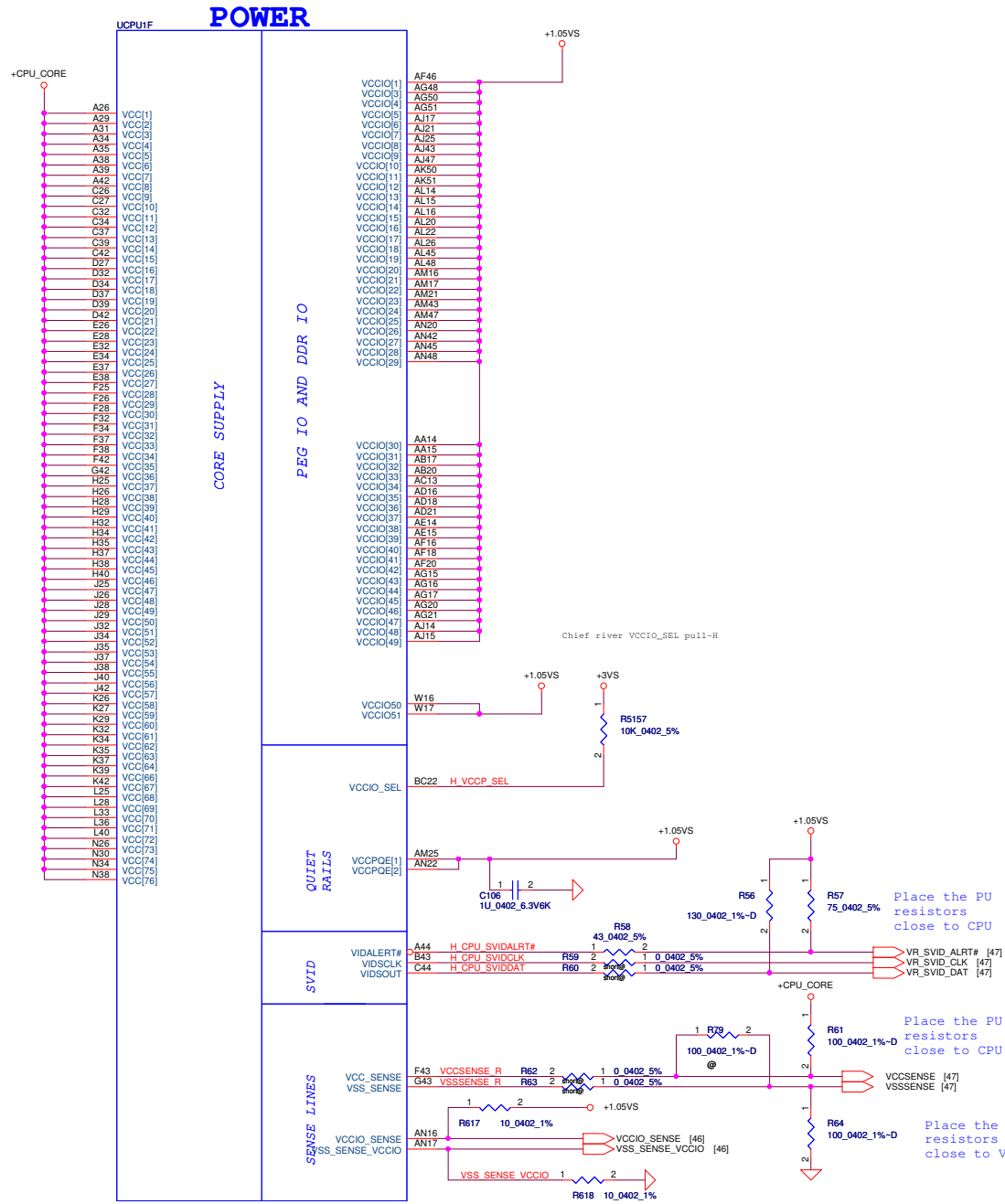
|      |   |
|------|---|
| CFG7 | <p>*1: (Default) PEG Train immediately following xxRESETB de assertion</p> <p>0: PEG Wait for BIOS for training</p> |
|------|---|

|   |                    |                 |            |                          |                         |       |
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CPU\_CORE  
GFX\_CORE  
VCCP1.05

VGA\_CORE

All Capacitor place on Power side.



|   |            |                    |            |                          |                            |
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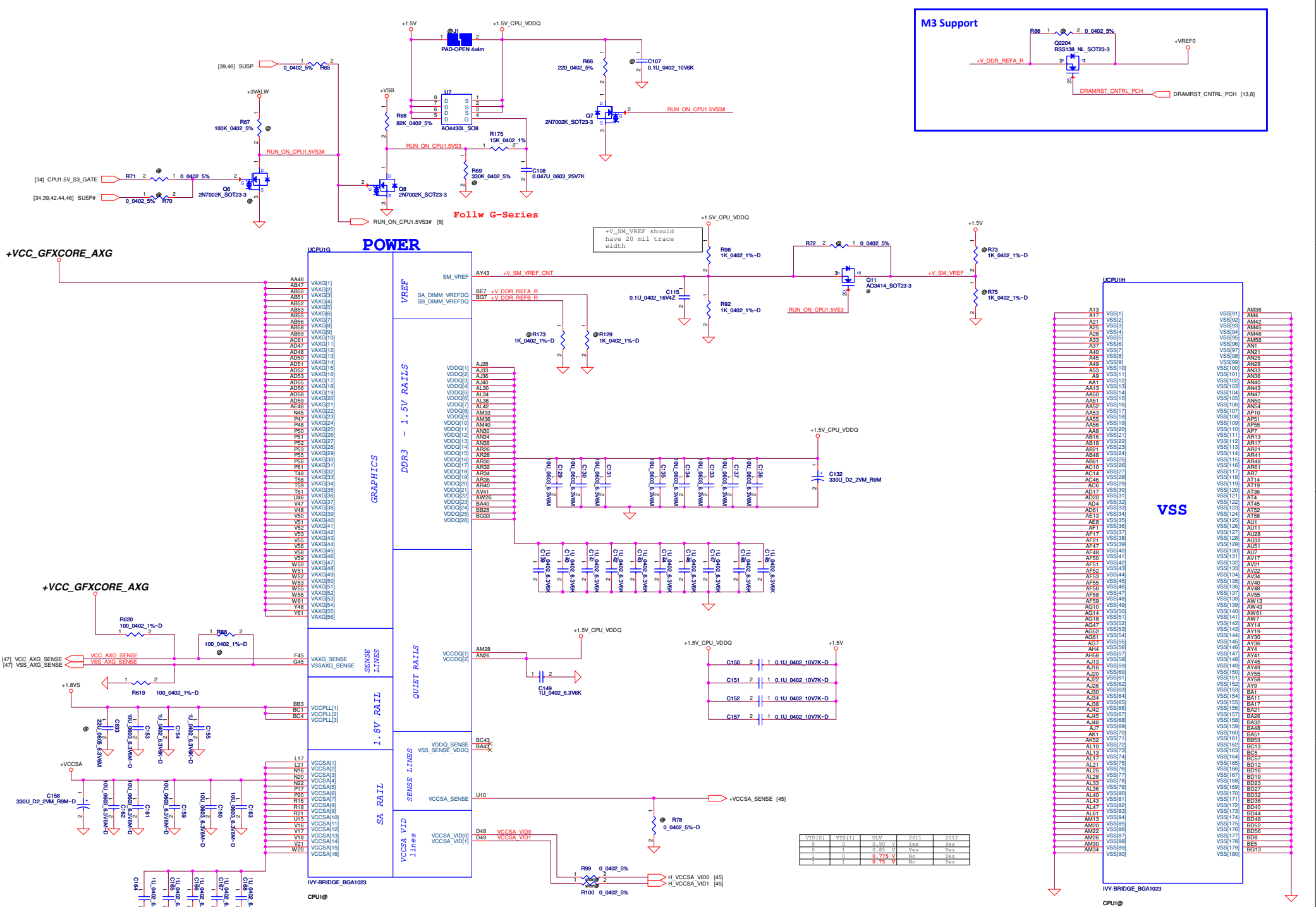
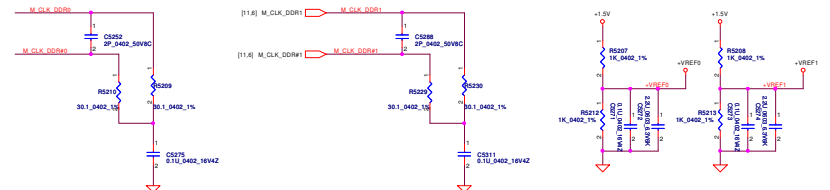
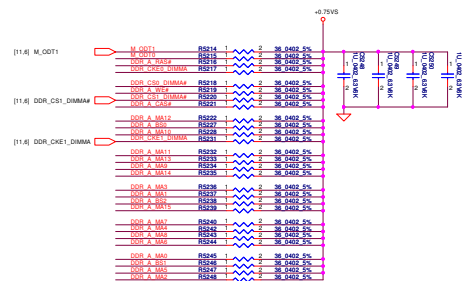
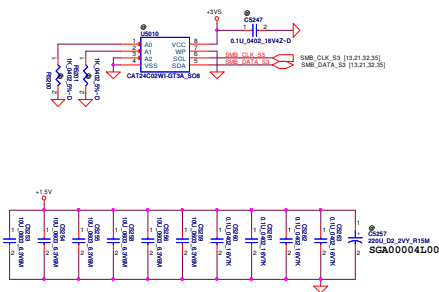
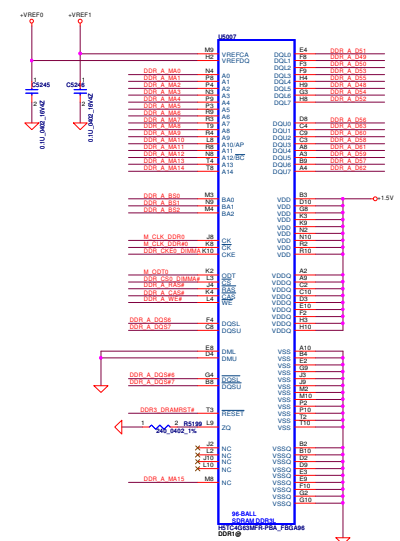
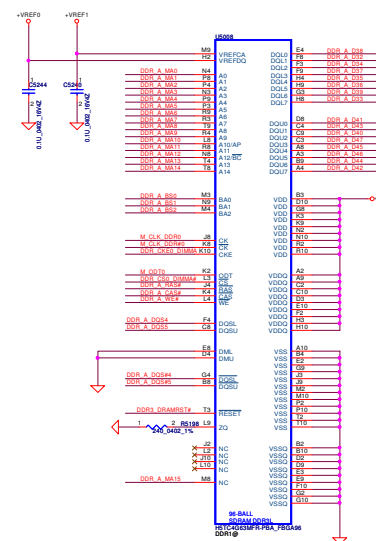
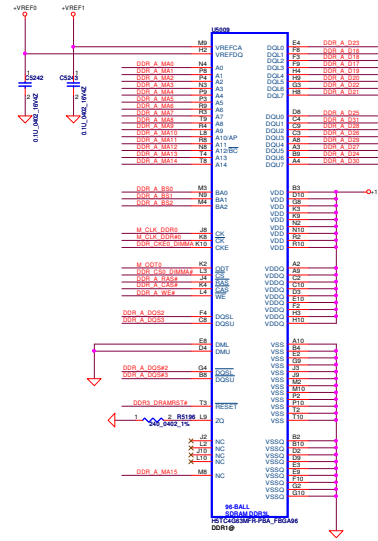
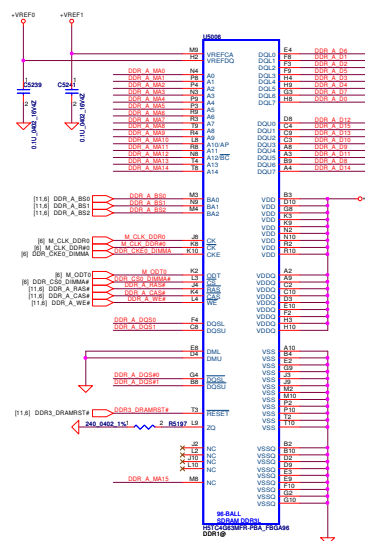


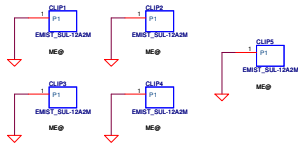
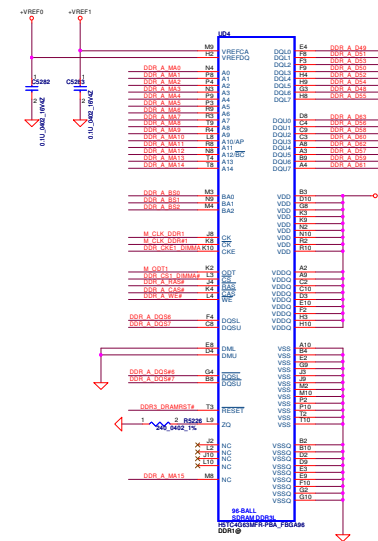
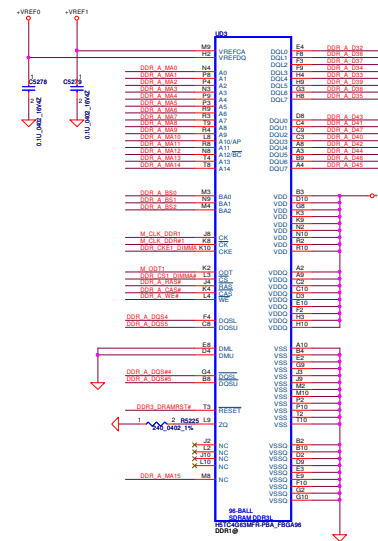
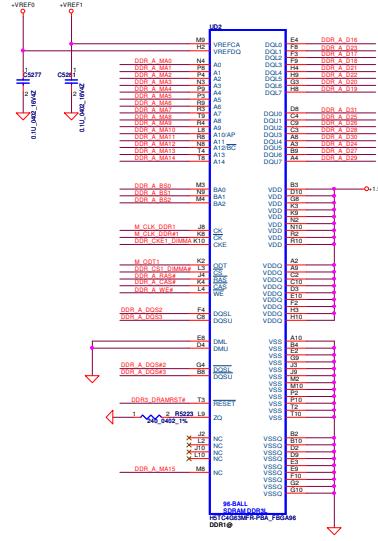
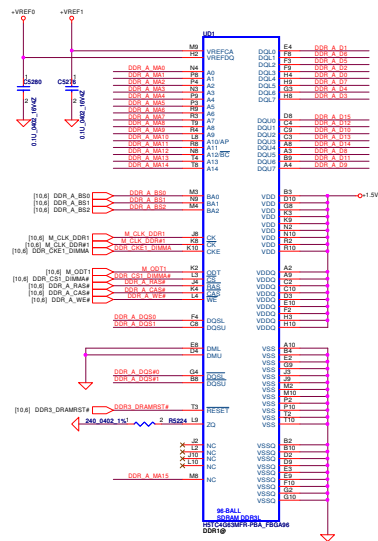
Diagram illustrating the overlapping ranges of DDR\_A signals:

- [11,6] DDR\_A\_M0[0..15]
- [11,6] DDR\_A\_DQS#0[0..7]
- [11,6] DDR\_A\_DQS0[0..7]
- [11,6] DDR\_A\_D0[0..63]



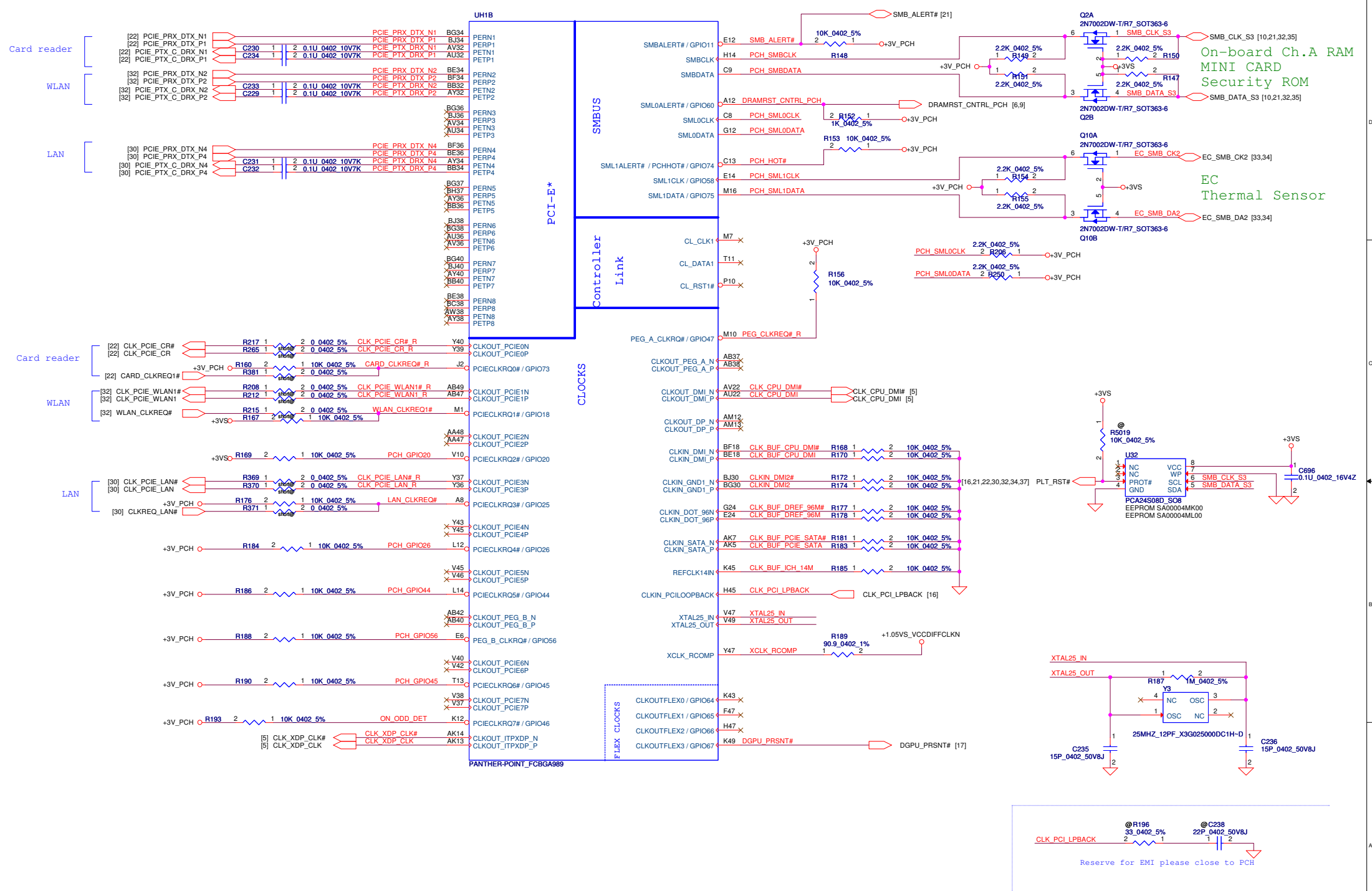
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[10.6] DDR\_A\_M0D-15  
 [10.6] DDR\_A\_DQ60-7  
 [10.6] DDR\_A\_DQ60-7  
 [10.6] DDR\_A\_DQ60-7  
 [10.6] DDR\_A\_DQ60-7

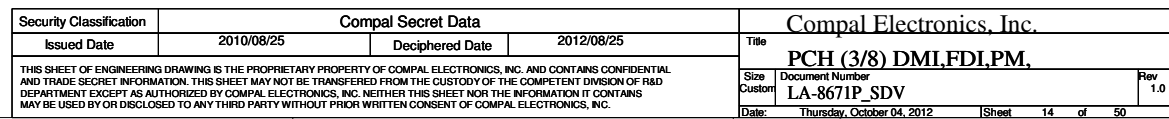
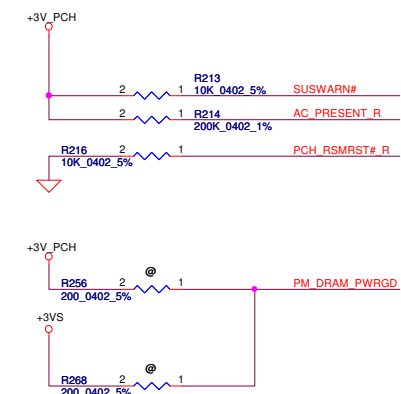


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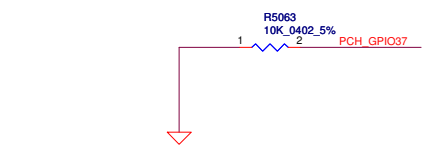
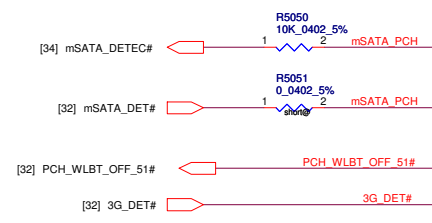
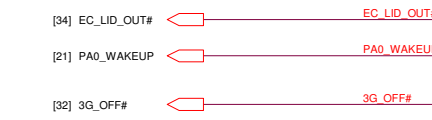
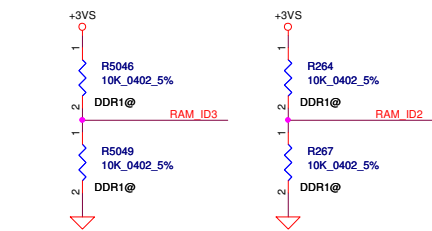
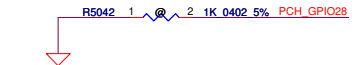




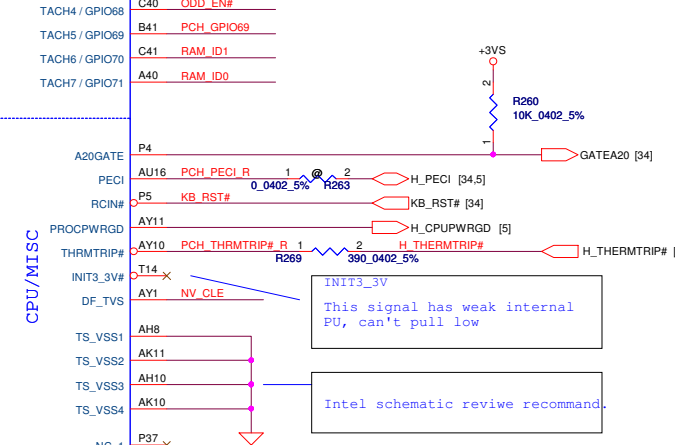
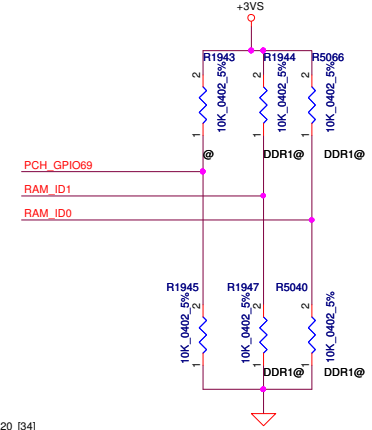
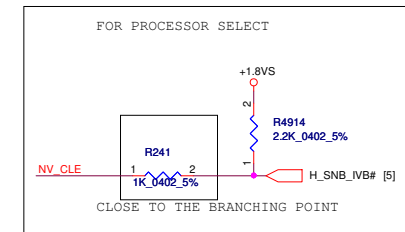
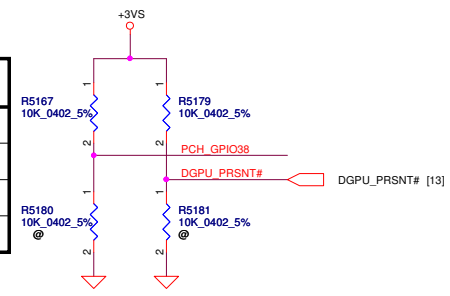
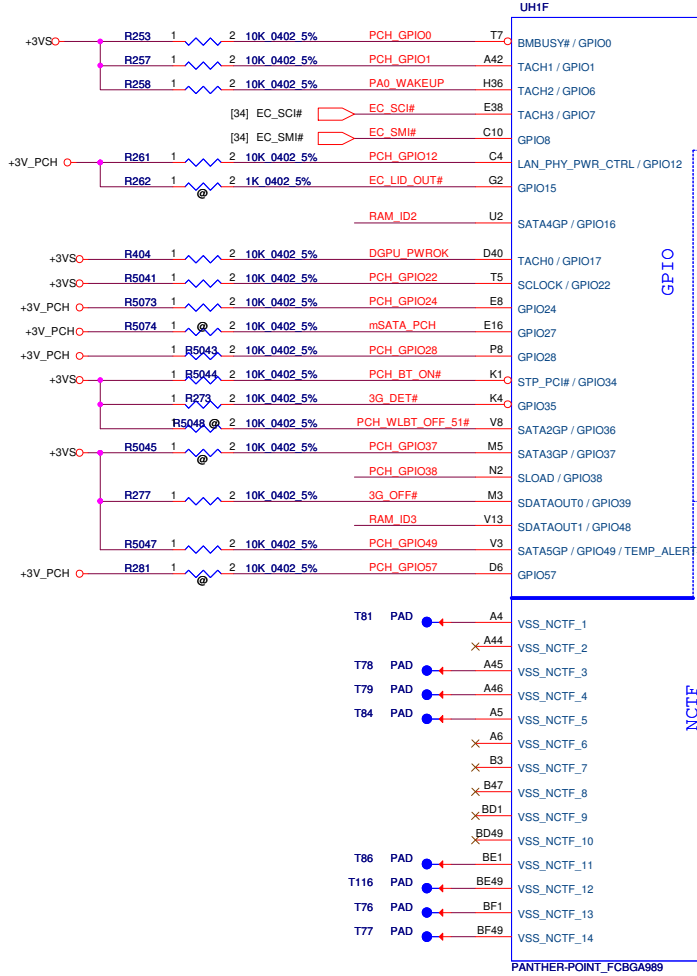


GPIO28  
On-Die PLL Voltage Regulator  
This signal has a weak internal pull up

★ H : On-Die voltage regulator enable  
L : On-Die PLL Voltage Regulator disable



| PCH_GPIO69 | PCH_GPIO38 | PCH_GPIO67 | Function | GPU_ID |
|------------|------------|------------|----------|--------|
| 0          | 0          | 0          | PX4.0    | 0      |
| 0          | 0          | 1          | Reserved | 1      |
| 0          | 1          | 0          | DIS      | 2      |
| 0          | 1          | 1          | UMA      | 3      |



| RAM_ID3 | RAM_ID2 | RAM_ID1 | RAM_ID0 | RAM         |
|---------|---------|---------|---------|-------------|
| GPIO48  | GPIO16  | GPIO70  | GPIO71  |             |
| 0       | 0       | 0       | 0       | ELPIDA 4GB  |
| 0       | 0       | 0       | 1       | SAMSUNG 4GB |
| 0       | 0       | 1       | 0       | HYNIX 4GB   |
| 0       | 0       | 1       | 1       | ELPIDA 8GB  |
| 0       | 1       | 0       | 0       | SAMSUNG 8GB |
| 0       | 1       | 0       | 1       | HYNIX 8GB   |
| 0       | 1       | 1       | 0       | ELPIDA 2GB  |
| 0       | 1       | 1       | 1       | SAMSUNG 2GB |
| 1       | 0       | 0       | 0       | HYNIX 2GB   |
| 1       | 0       | 0       | 1       | TBD         |
| 1       | 0       | 1       | 0       | TBD         |
| 1       | 0       | 1       | 1       | TBD         |
| 1       | 1       | 0       | 0       | TBD         |
| 1       | 1       | 0       | 1       | TBD         |
| 1       | 1       | 1       | 0       | TBD         |
| 1       | 1       | 1       | 1       | TBD         |

Security Classification

Issued Date

Compal Secret Data

2010/08/25

Deciphered Date

2012/08/25

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Title

Document Number

Rev

Date

Thursday, October 04, 2012

Sheet

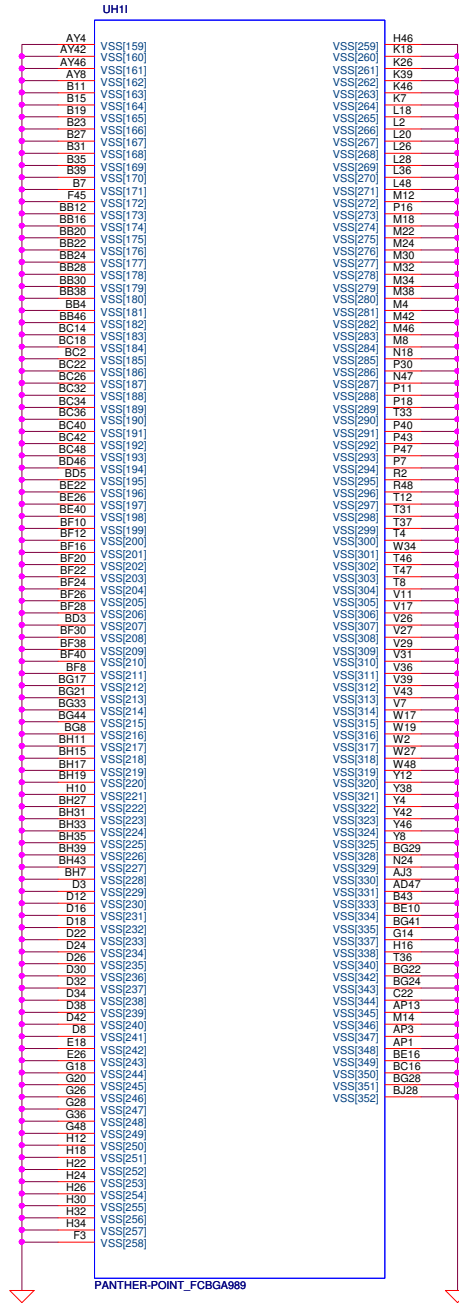
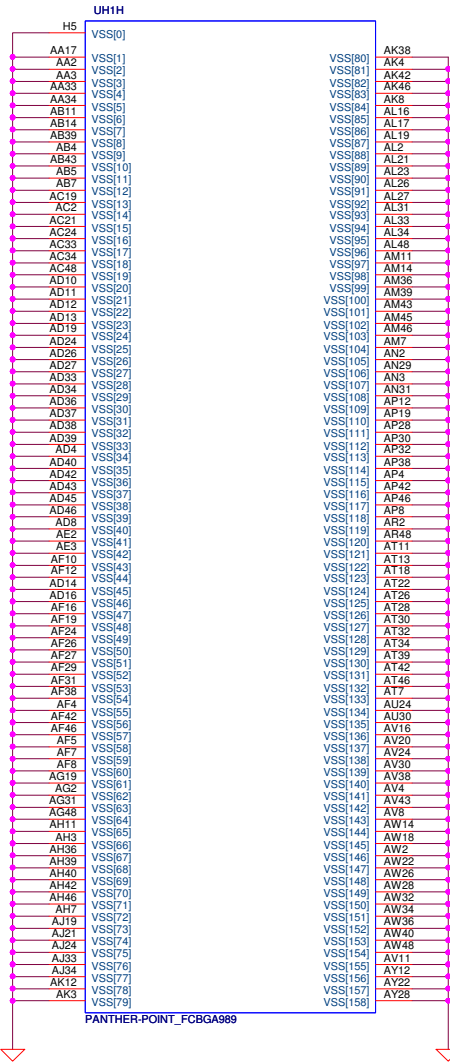
17

of

50

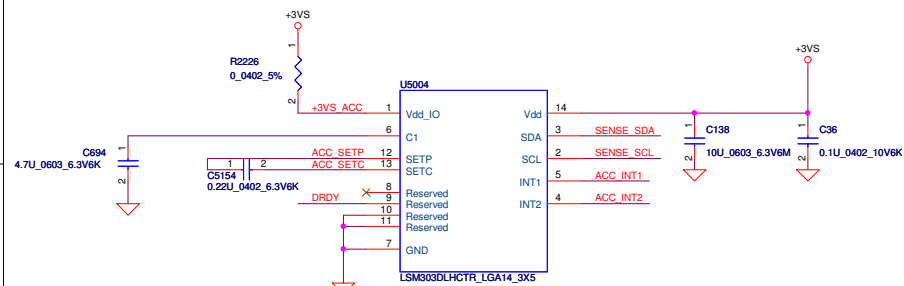




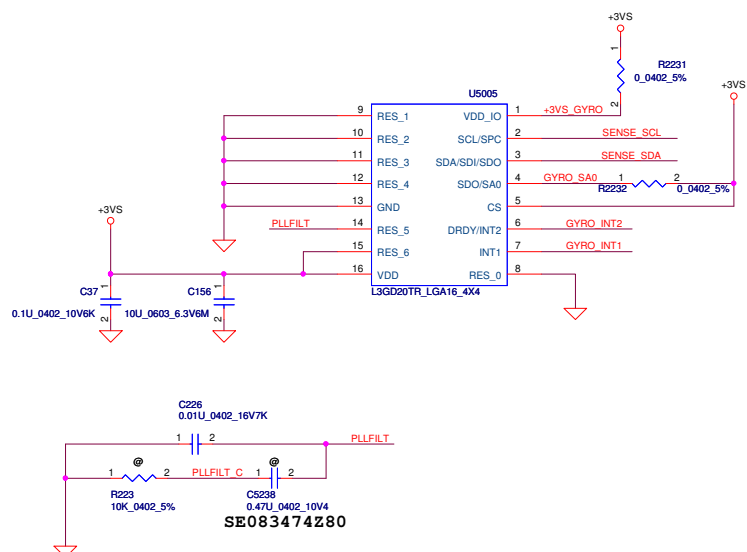


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| Issued Date             |  | 2010/08/25 |  | Deciphered Date    |  | 2012/08/25 |  | Title                    |  |                            |  |
|                         |  |            |  |                    |  |            |  | PCH (9/9) VSS            |  |                            |  |
|                         |  |            |  |                    |  |            |  | Size                     |  | Document Number            |  |
|                         |  |            |  |                    |  |            |  | LA-8671P_SDV             |  | Rev                        |  |
|                         |  |            |  |                    |  |            |  | Date:                    |  | Thursday, October 04, 2012 |  |
|                         |  |            |  |                    |  |            |  | Sheet                    |  | 20 of 50                   |  |

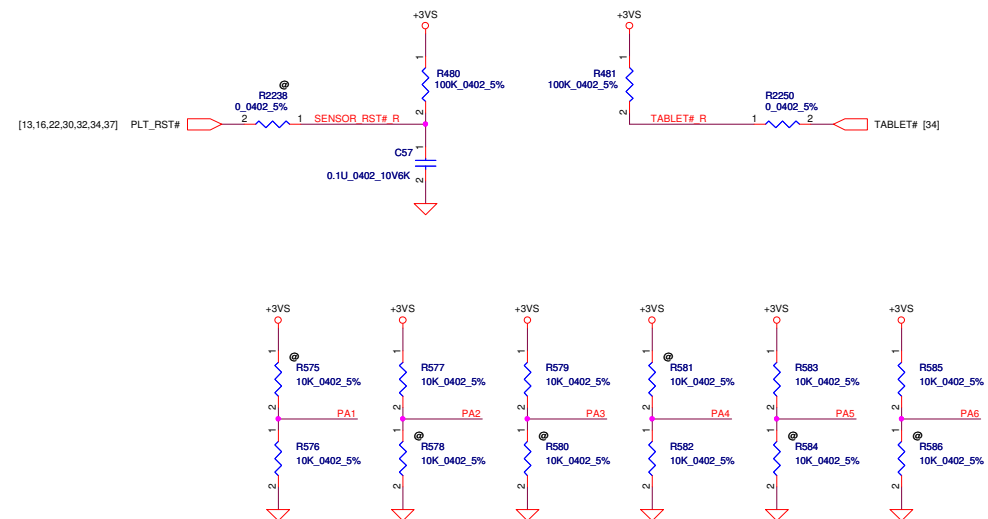
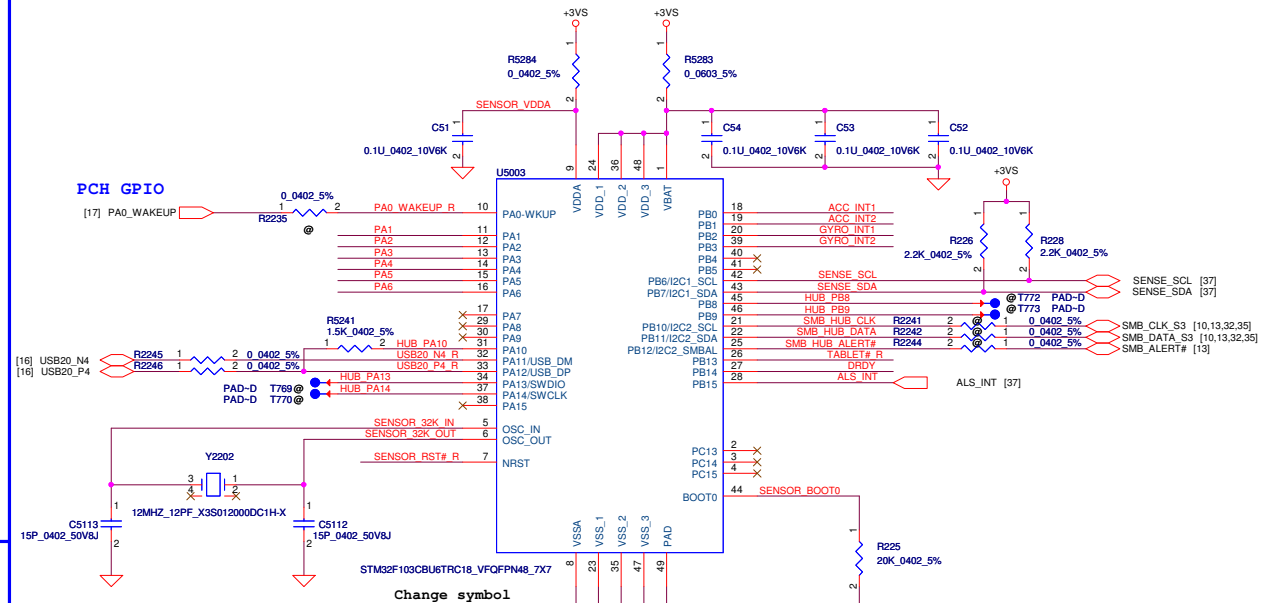
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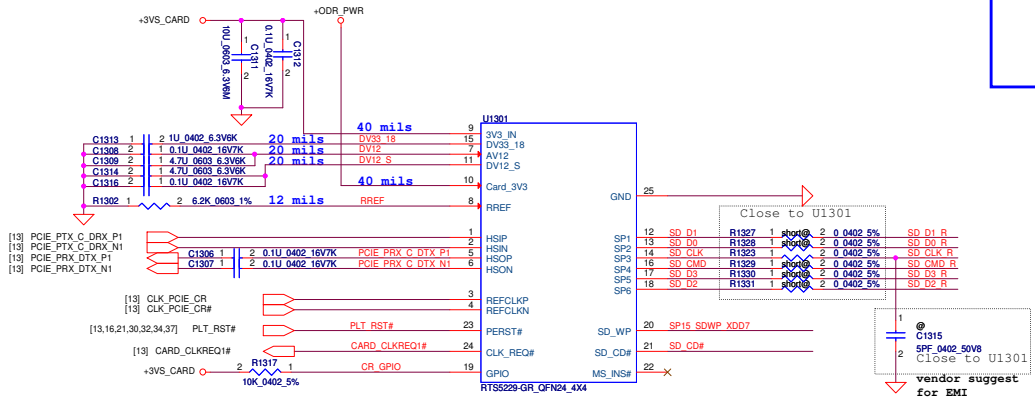
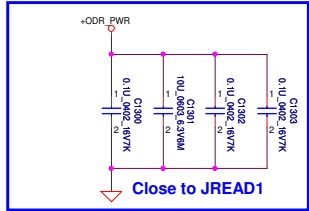
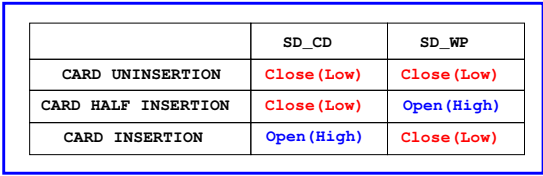
Gyro



## Sensor Hub



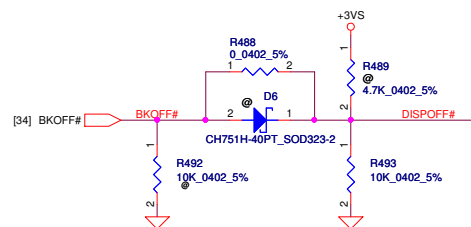
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|   |            |                            |            | Size                     | Document Number | Rev |    |
|   |            |                            |            | Custom                   | LA-8671P SDV    | 1.0 |    |
| Date:   |            | Thursday, October 04, 2012 |            | Sheet                    | 21              | of  | 50 |



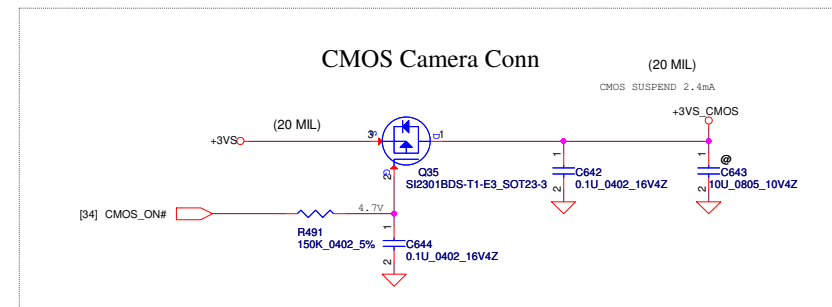
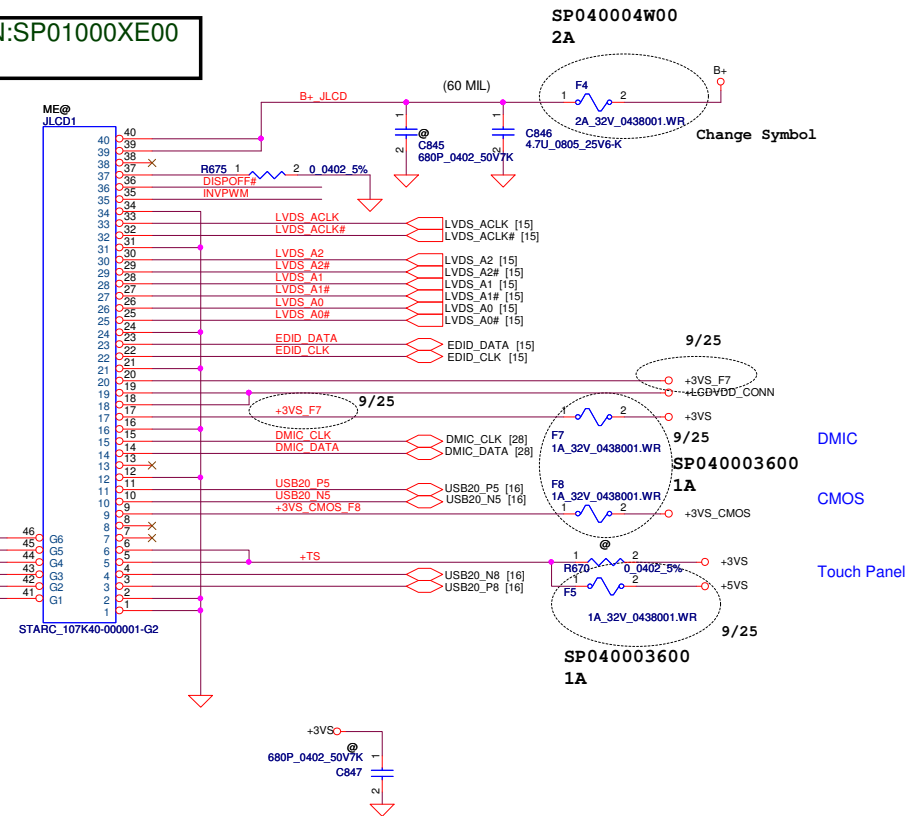
W=60mils

## LCD POWER CIRCUIT

For GMCH DPST

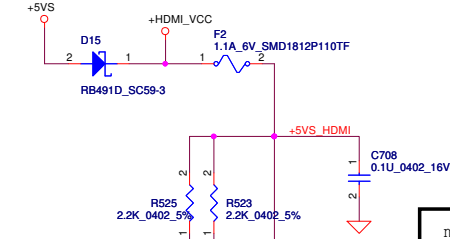
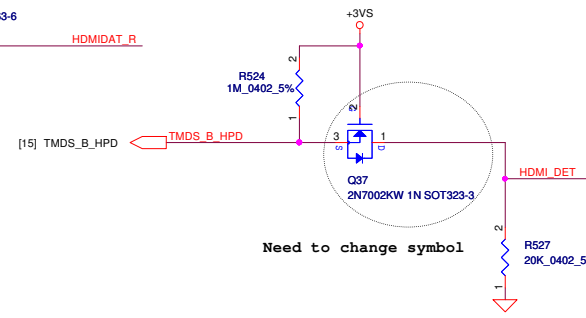
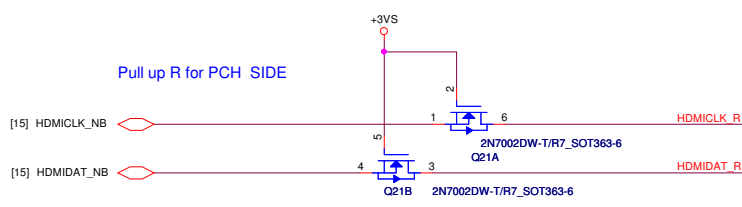
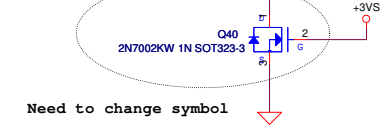
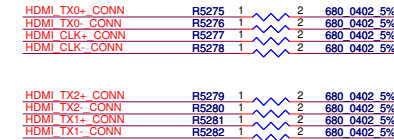
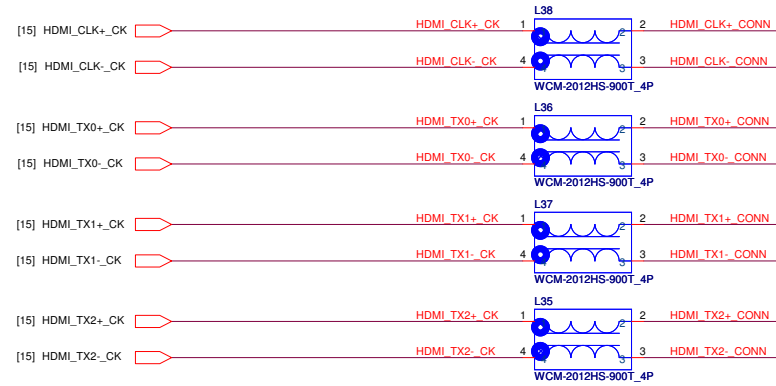


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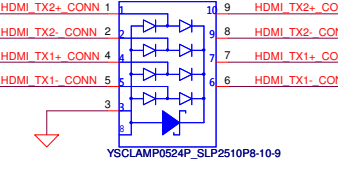
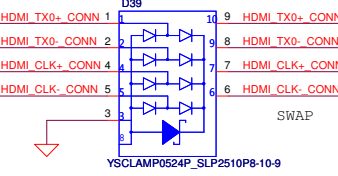
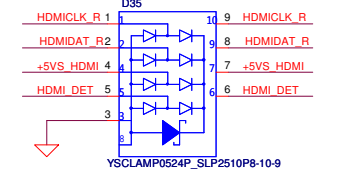


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|   |                            |                    |            | Document Number          | 1A          |
|   |                            |                    |            | LA-8671P_SDV             |             |
| Date:   | Thursday, October 04, 2012 | Sheet              | 23         | of                       | 50          |

# PN:SM070001S00x 4



Type-C  
mHDMI PN:SP060005H00

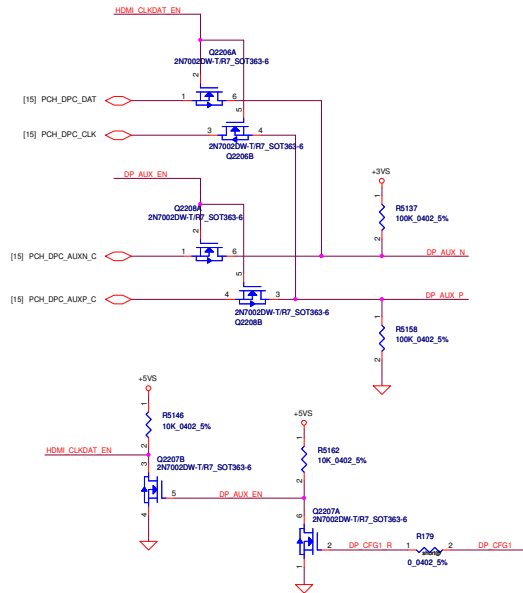
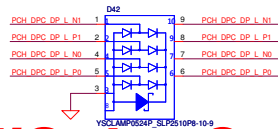
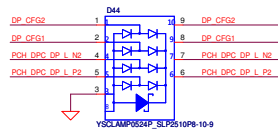
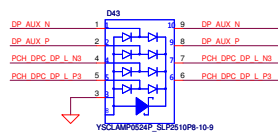
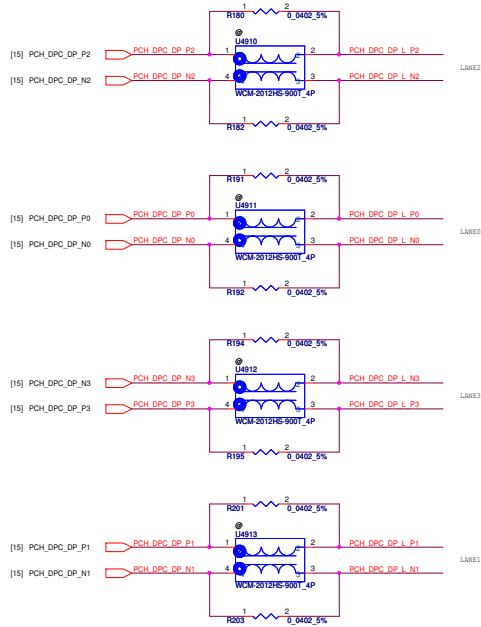


1'SPN: SC300001Y00  
2'SPN: SC300002800

|   |  |                    |                 |                          |           |                            |                |
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|   |  |                    |                 |                          | Size      | Document Number            | Rev            |
|   |  |                    |                 |                          | Custom    | LA-8671P_SDV               | 1.0            |
|   |  |                    |                 |                          | Date:     | Thursday, October 04, 2012 | Sheet 24 of 50 |
| 3   |  | 2                  |                 | 1                        |           |                            |                |

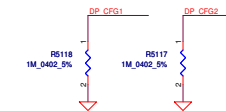


Close to JmDP1

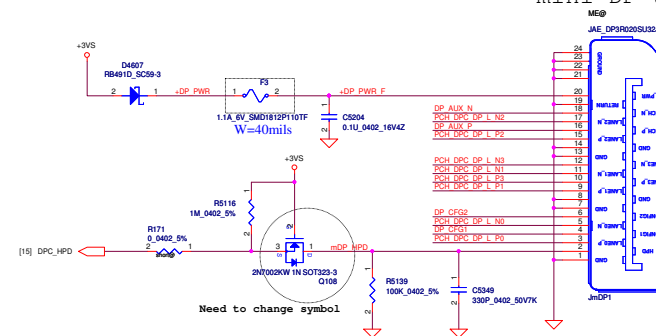


TBT, HDMI, DP mode table

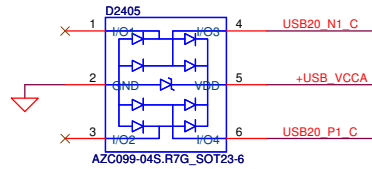
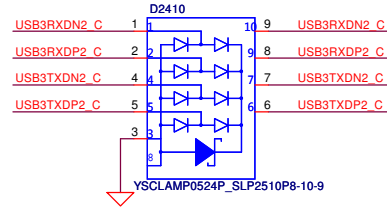
| HPD | CFG1 | CFG2 | LSRX | Mode |
|-----|------|------|------|------|
| 1   | 0    | 0    | X    | DP   |
| 1   | 1    | X    | X    | HDMI |
| 0   | 0    | 1    | 1    | TBT  |



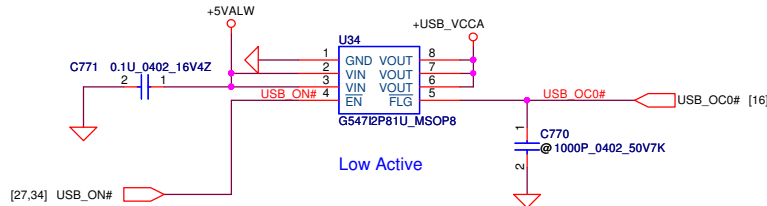
mini DP type



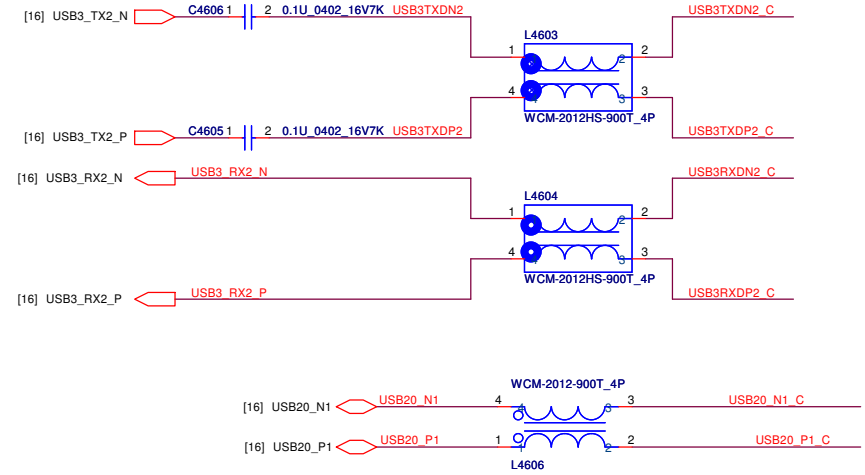
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2'SPN: SC300002800



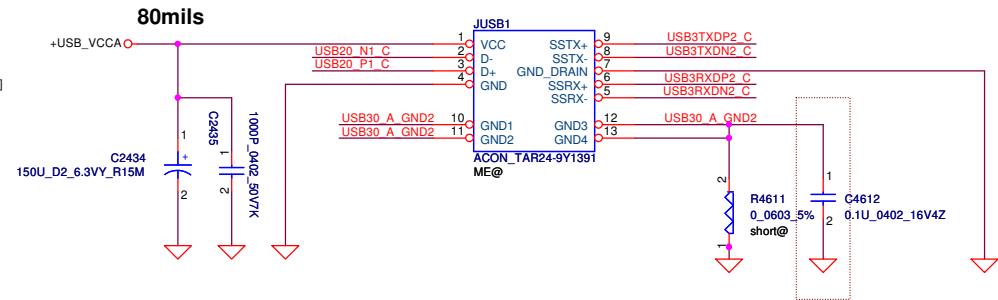
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2'S PN: SC300002E00



## JUSB1USB3.0/USB2.0

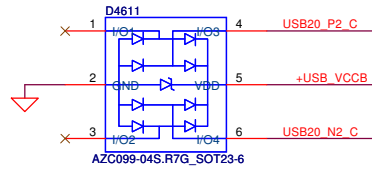
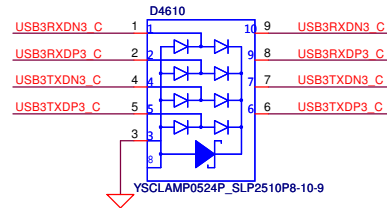


## DC23300A410

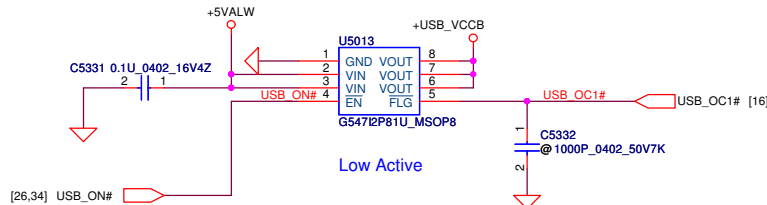


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| Security Classification   |                            | Compal Secret Data |              | Compal Electronics, Inc. |     |
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| Size  | Custom                     | Document Number    | LA-8671P_SDV | Rev                      | 1.0 |
| Date:   | Thursday, October 04, 2012 | Sheet              | 26           | of                       | 50  |

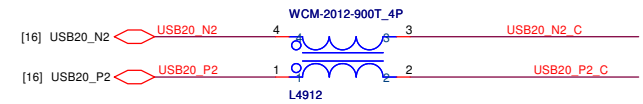
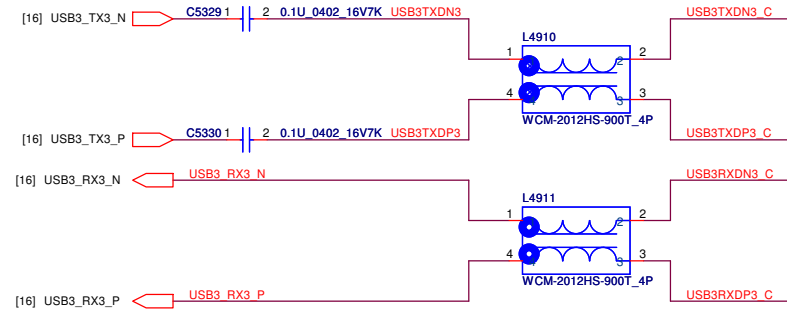
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2'SPN: SC300002800



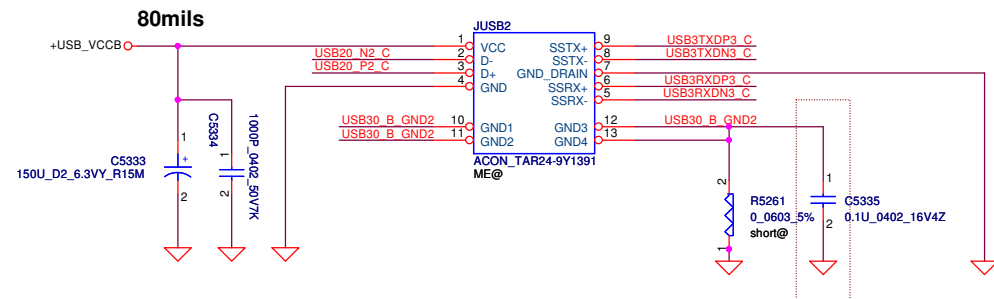
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2'S PN: SC300002E00



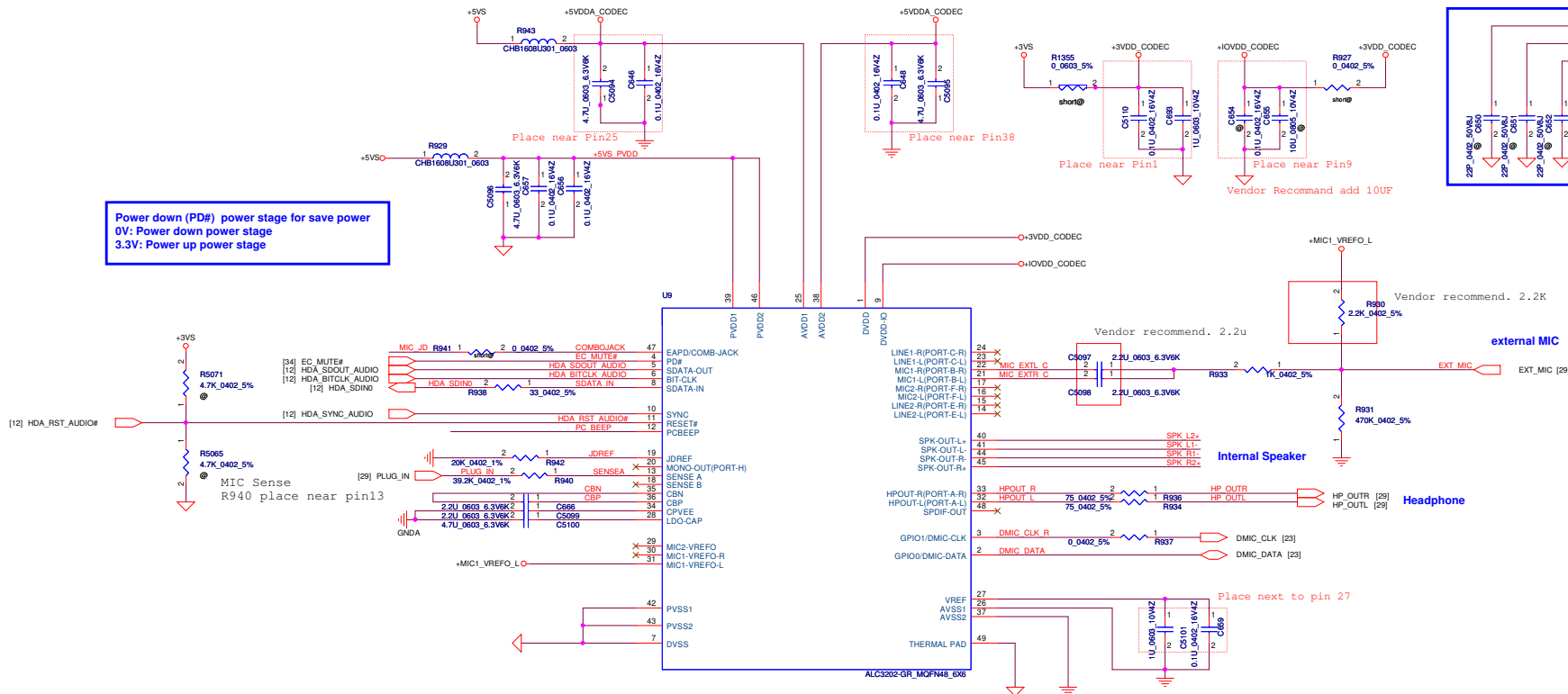
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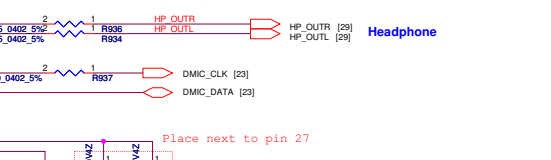
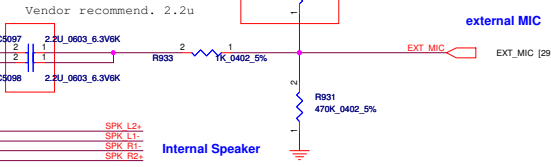
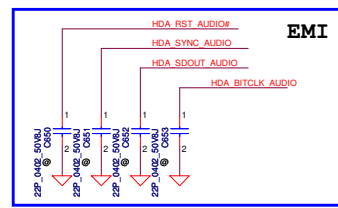
DC23300A410



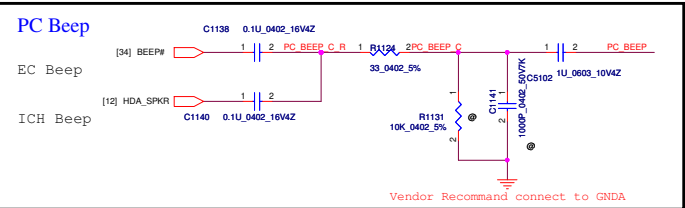
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| Size  | Custom                     | Document Number    | LA-8671P_SDV | Rev                      | 1.0 |
| Date:   | Thursday, October 04, 2012 | Sheet              | 27           | of                       | 50  |



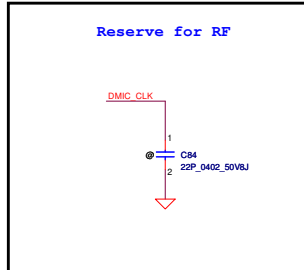
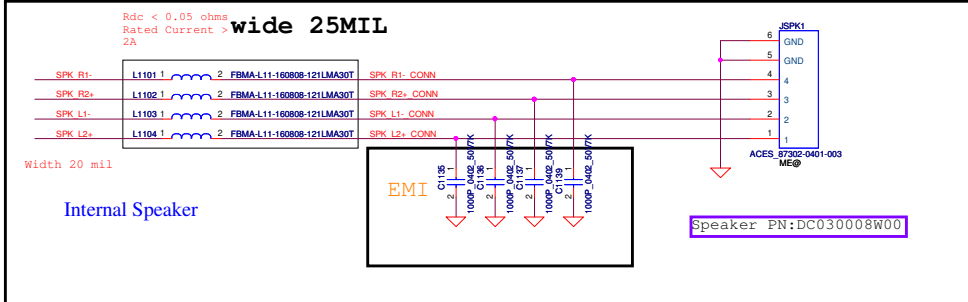
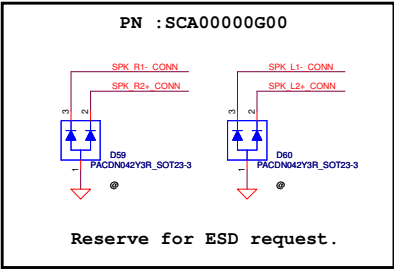
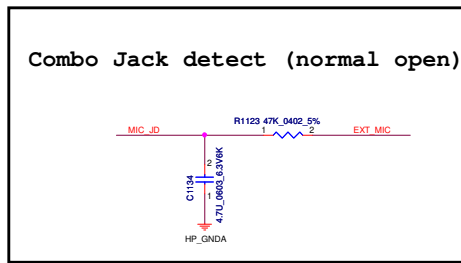
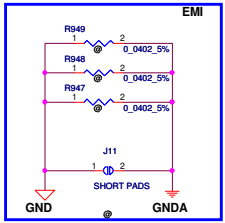
Power down (PD#) power stage for save power  
0V: Power down power stage  
3.3V: Power up power stage



SA000058310  
Change symbol

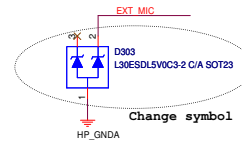
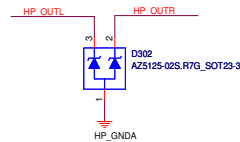
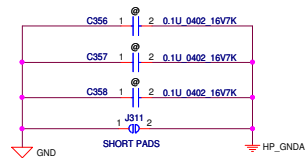
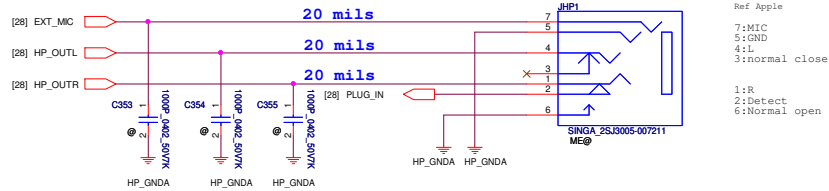


| Pin Assignment            | Location | Function      |
|---------------------------|----------|---------------|
| SPK-OUT (Pin40/41/44/45)  | Internal | Int Speaker   |
| Capless HP-OUT (Pin32/33) | External | Headphone out |
| MIC1 (Pin21/22)           | External | Mic in        |

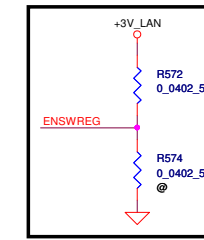
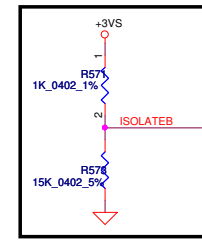
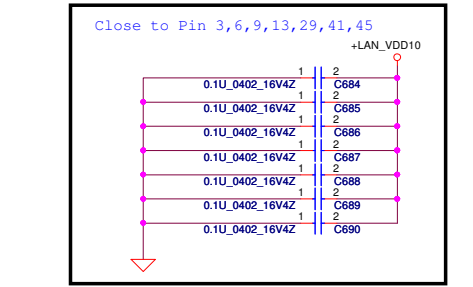
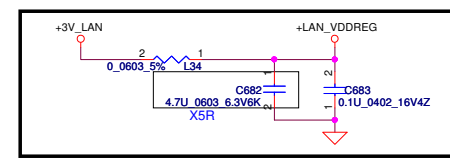
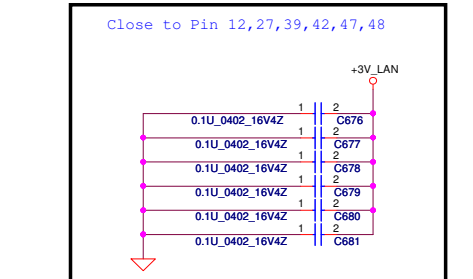
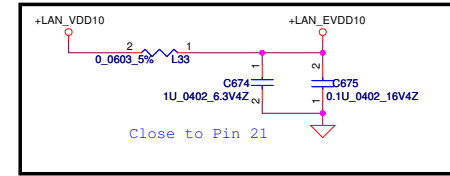
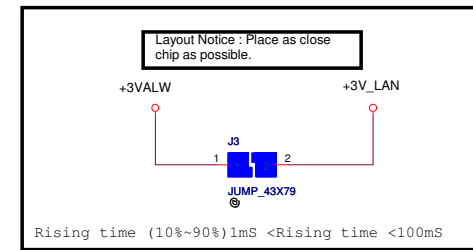
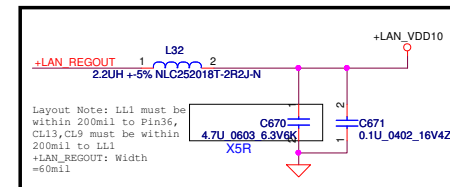
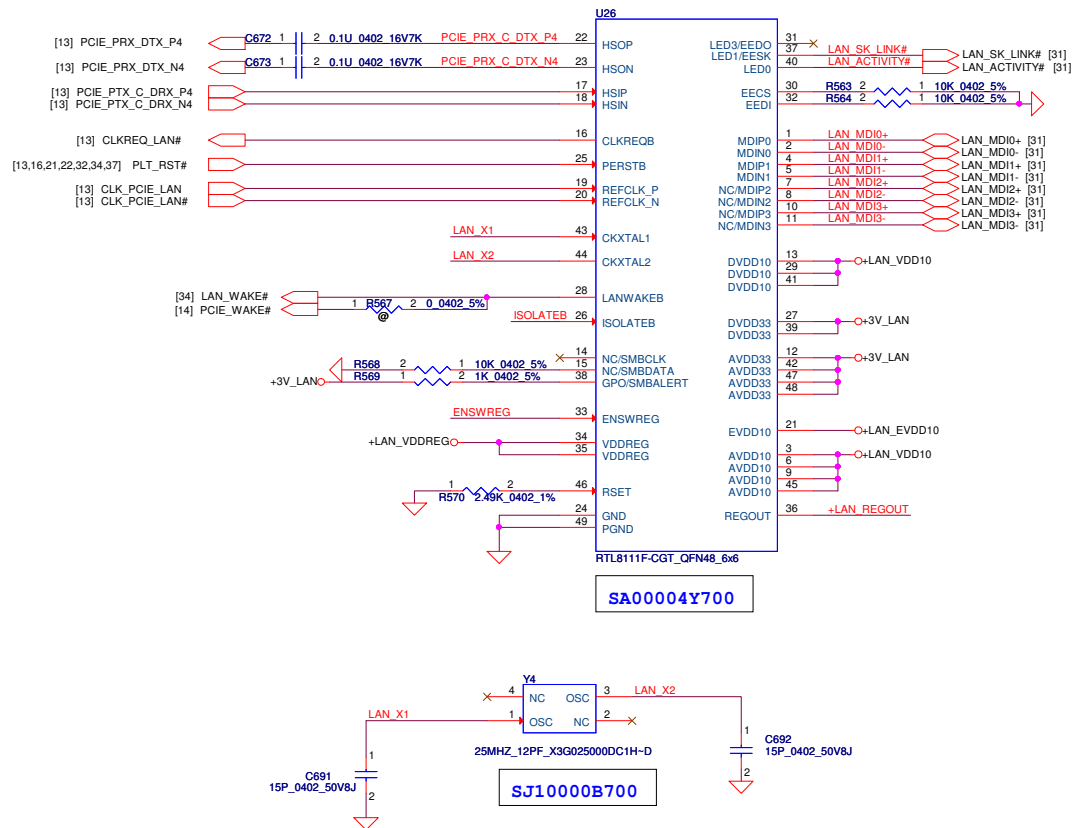


PN:DC230008E00

Choose normal-open type!

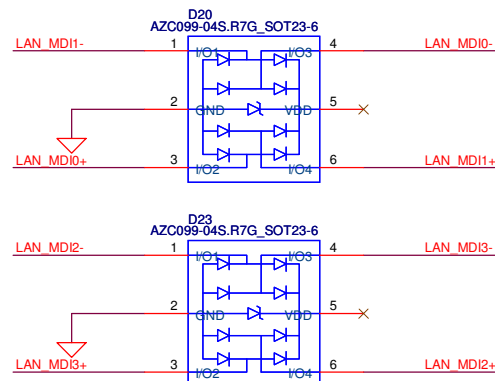
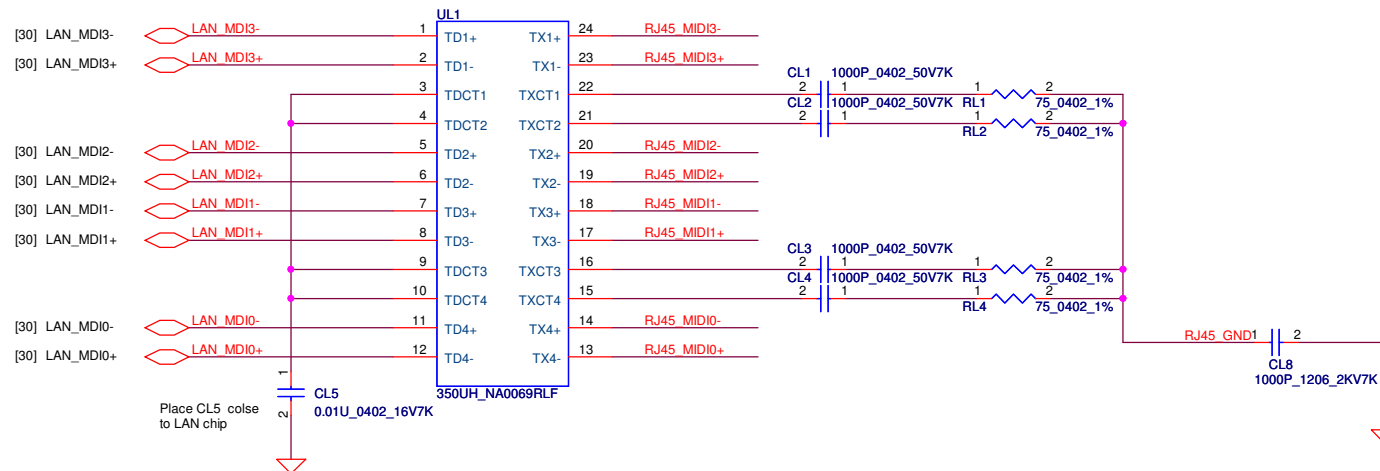


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|---|--------------------|-----------------|------------|--------------------------|---|
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| Issued Date   | 2011/05/16         | Deciphered Date | 2013/05/16 | Title                    | Combo Jack                                      |
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|   |                    |                 |            |                          | Document Number<br>LA-8671P_SDV                 |
|   |                    |                 |            |                          | Rev<br>1.0                                      |
|   |                    |                 |            |                          | Date: Thursday, October 04, 2012 Sheet 29 of 50 |



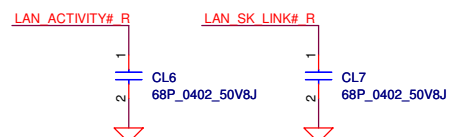
H: Enable internal Regular  
L: Disable

| Security Classification   |  |  |  | Compal Secret Data         |  |  |  | Compal Electronics, Inc. |  |  |  |
|---|--|--|--|----------------------------|--|--|--|--------------------------|--|--|--|
| Issued Date   |  |  |  | 2010/08/25                 |  |  |  | Title                    |  |  |  |
| Deciphered Date   |  |  |  | 2012/08/25                 |  |  |  | LAN-RTL8111F             |  |  |  |
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| Date:   |  |  |  | Thursday, October 04, 2012 |  |  |  | Sheet                    |  |  |  |
| 30  |  |  |  | of                         |  |  |  | 50                       |  |  |  |



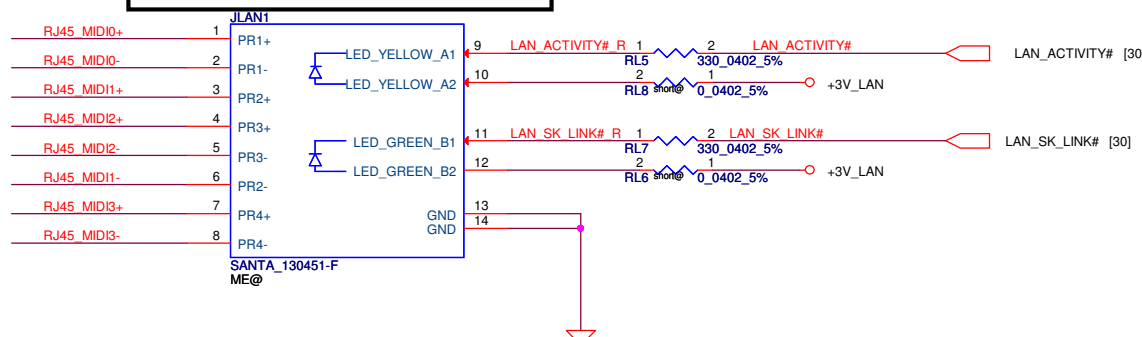
D20/D23  
1'S PN:SC300001G00  
2'S PN:SC300002E00

For EMI's request



DC234005300  
LAN Conn.

RL5,RL7 Vendor recommend 510 ohm



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|---|------------|--------------------|------------|----------------------------------|-----------------|
| Issued Date   | 2010/08/25 | Deciphered Date    | 2012/08/25 | Title                            |                 |
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|   |            |                    |            | Size Custom                      | Document Number |
|   |            |                    |            | LA-8671P_SDV                     |                 |
|   |            |                    |            | Date: Thursday, October 04, 2012 | Sheet 31 of 50  |
|   |            |                    |            | Rev 1.0                          |                 |

## Mini-Express Card(WLAN/BT Combo)

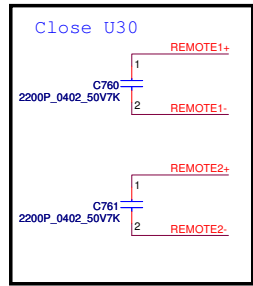


Close to JMIN2 on the same side

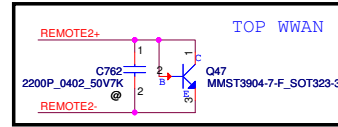
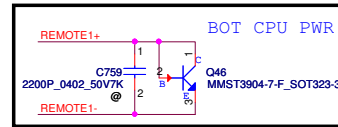
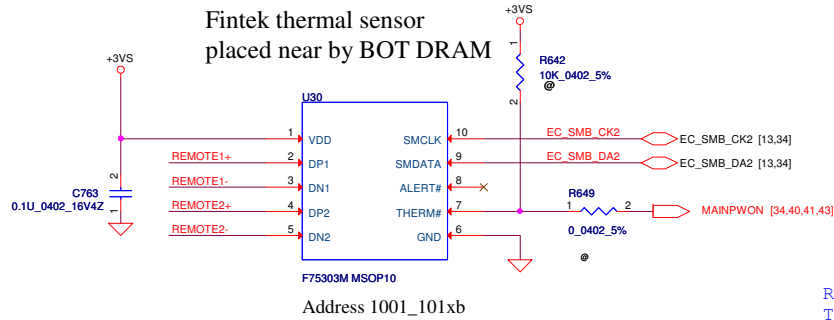
**AOAC**

|   |                            |                    |            |                          |                     |
|---|----------------------------|--------------------|------------|--------------------------|---------------------|
| Security Classification   |                            | Compal Secret Data |            | Compal Electronics, Inc. |                     |
| Issued Date   | 2010/08/25                 | Deciphered Date    | 2012/08/25 | Title                    | WLAN/BT/WWAN/m-SATA |
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|   |                            |                    |            | Custom<br>LA-8671P_SDV   |                     |
| Date:   | Thursday, October 04, 2012 | Sheet              | 32         | of                       | 50                  |



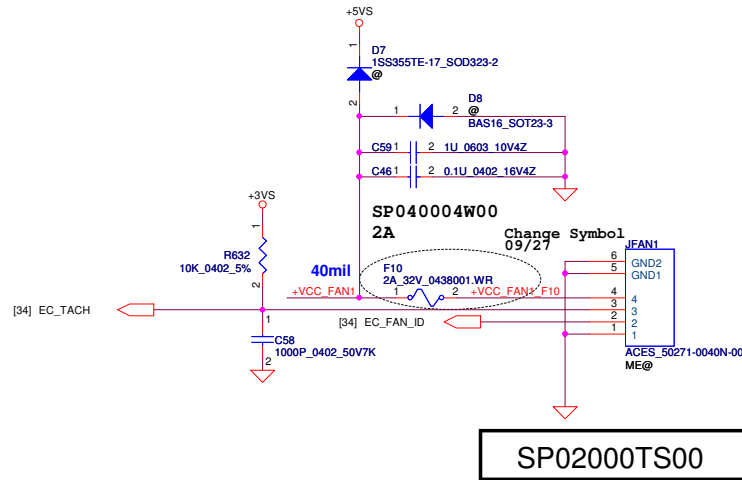
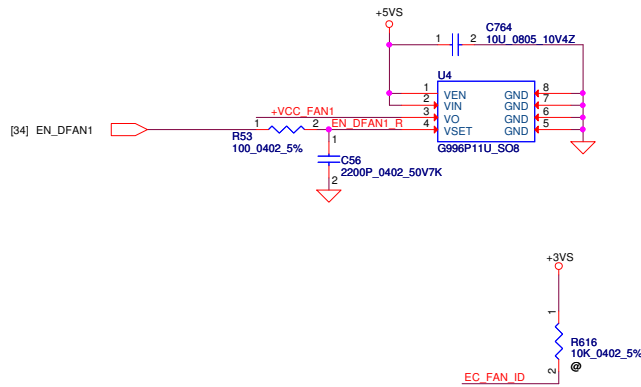


Fintek thermal sensor  
placed near by BOT DRAM

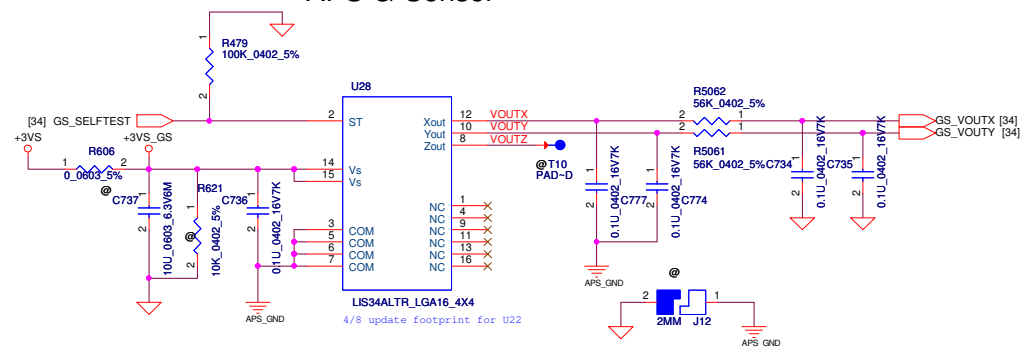
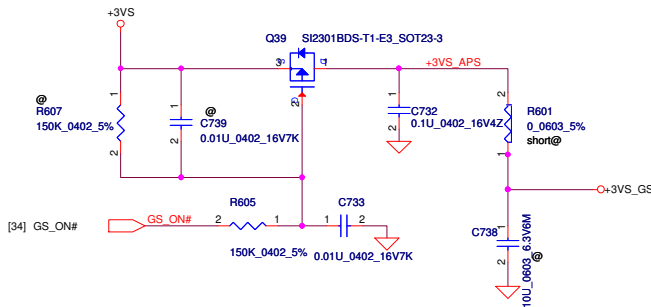


REMOTE1,2+/-:  
Trace width/space:10/10 mil  
Trace length:<8"

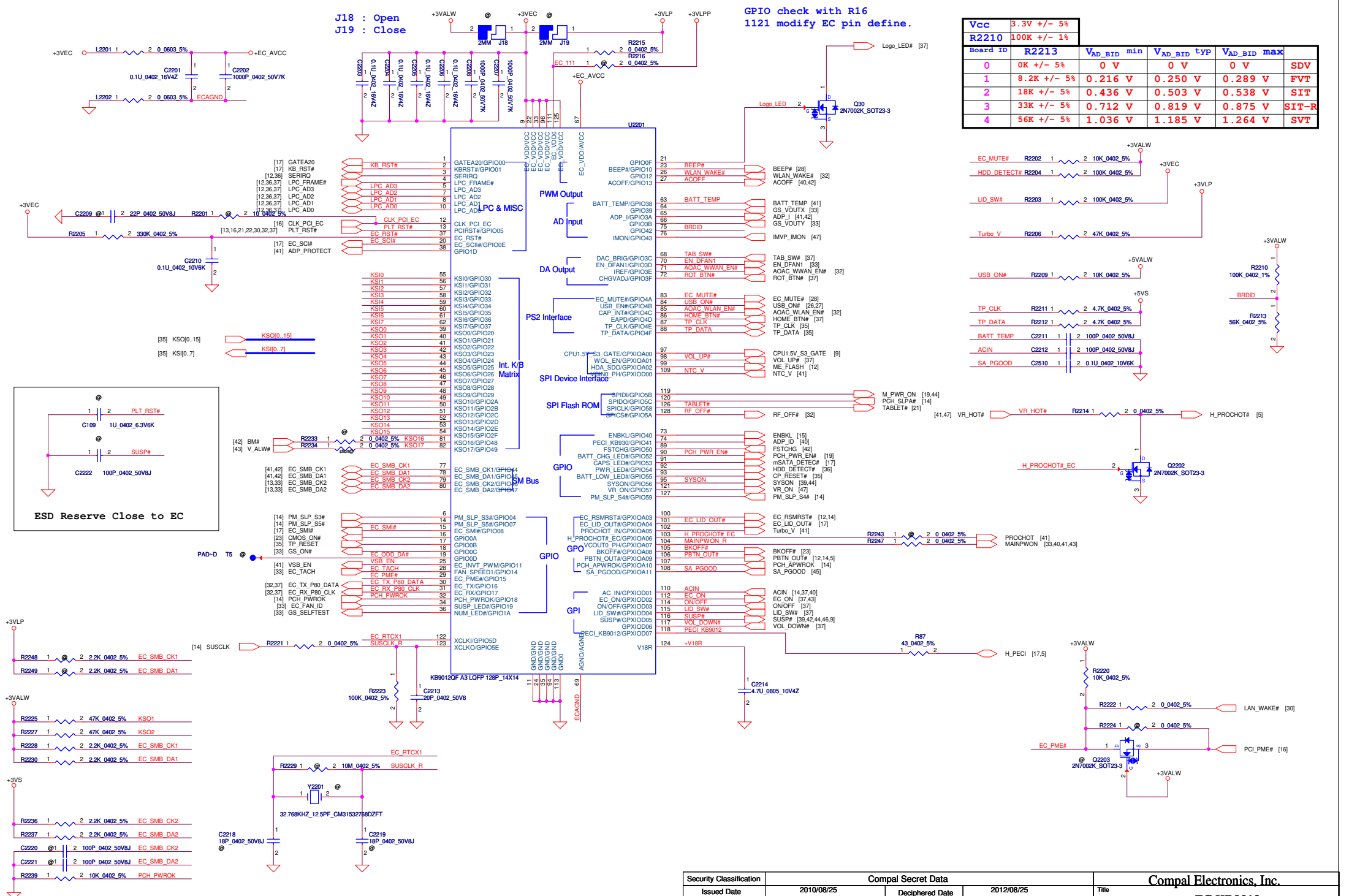
FAN +5VS DROOP



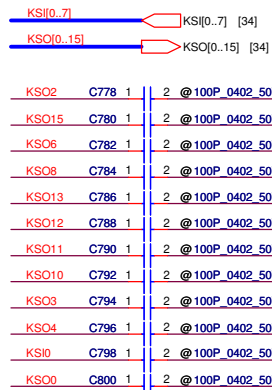
APS G-Sensor



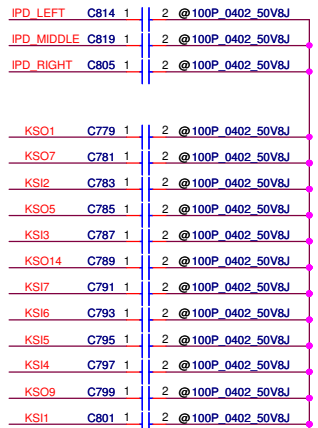
| Security Classification |  |  | Compal Secret Data |  |  | Compal Electronics,Ltd.          |  |  |
|-------------------------|--|--|--------------------|--|--|----------------------------------|--|--|
| Issued Date             |  |  | 2010/08/25         |  |  | Title                            |  |  |
|                         |  |  | Deciphered Date    |  |  | FintekF57303_ Thermal sensor/FAN |  |  |
|                         |  |  |                    |  |  | Size                             |  |  |
|                         |  |  |                    |  |  | Document Number                  |  |  |
|                         |  |  |                    |  |  | LA-8671P_SDV                     |  |  |
|                         |  |  |                    |  |  | Date: Thursday, October 04, 2012 |  |  |
|                         |  |  |                    |  |  | Sheet 33 of 50                   |  |  |



## INT\_KBD Conn.



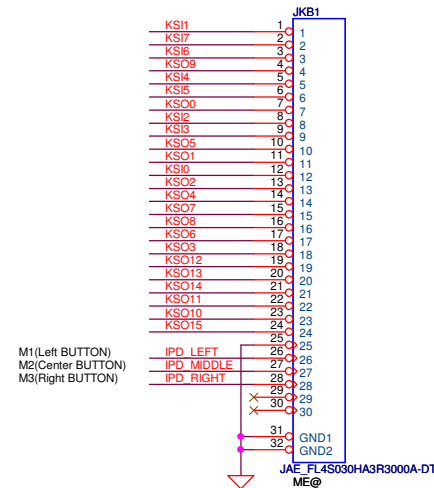
CONN PIN define need double check



Reserve for ESD.

## INT\_KBD Conn.

PN:SP01000YH00

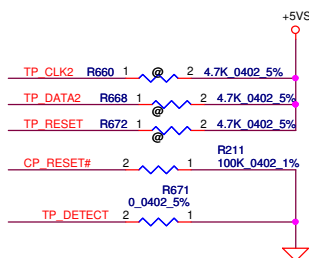
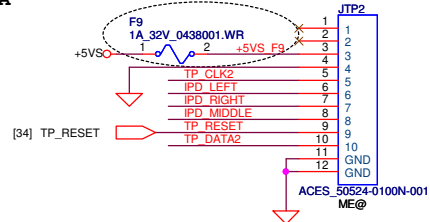


## Track point

PN:SP01001CH00

SP040003600  
1A

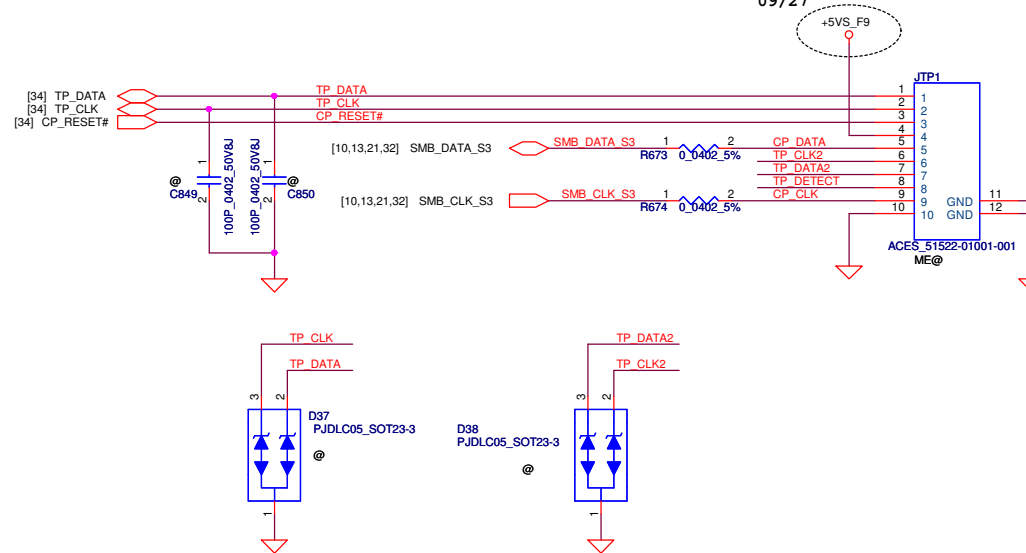
09/27



## Click pad 10PIN

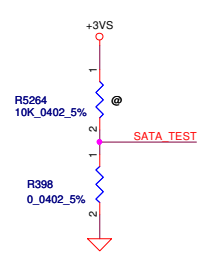
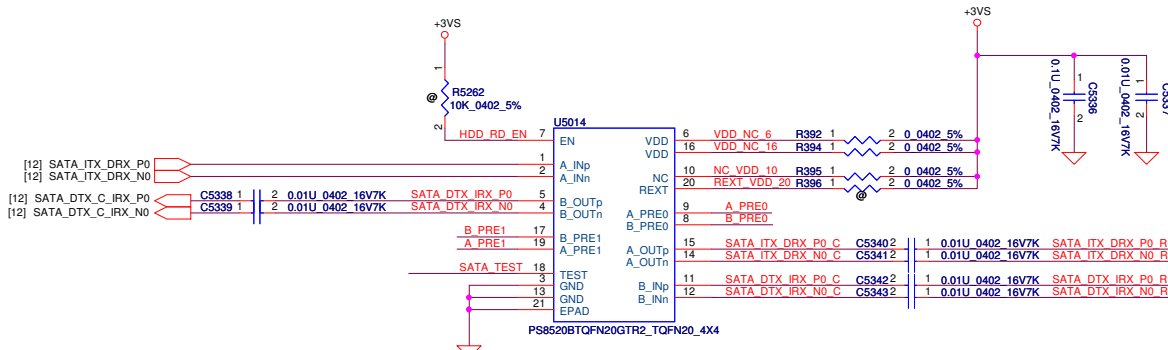
SP01001AL00

09/27

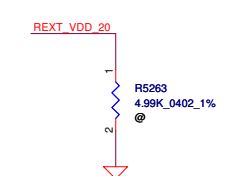


PN: SCA00000U10 X 2

|   |                    |                 |            |                          |                            |
|---|--------------------|-----------------|------------|--------------------------|----------------------------|
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|   |                    |                 |            | LA-8671P_SDV             |                            |
|   |                    |                 |            | Date:                    | Thursday, October 04, 2012 |
|   |                    |                 |            | Sheet                    | 35 of 50                   |
|   |                    |                 |            | Rev                      | 1.0                        |

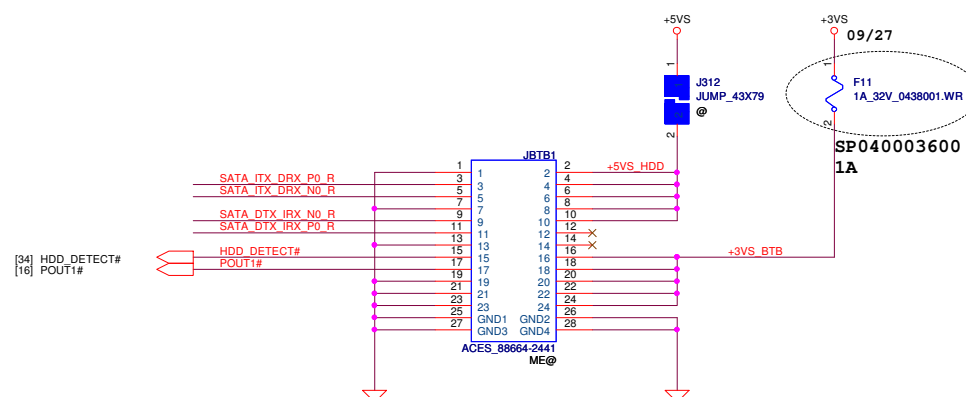
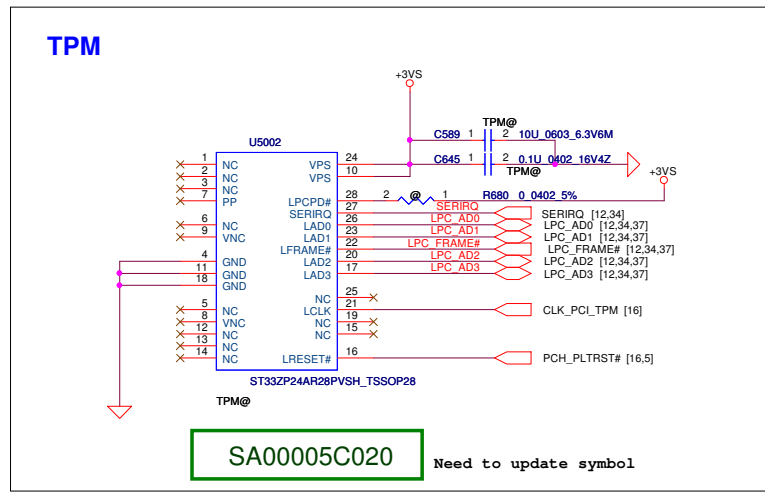


|                          | TEST<br>(Internal Low) |
|--------------------------|------------------------|
| Normal Mode<br>(Default) | LOW                    |
| Test Mode                | HIGH                   |

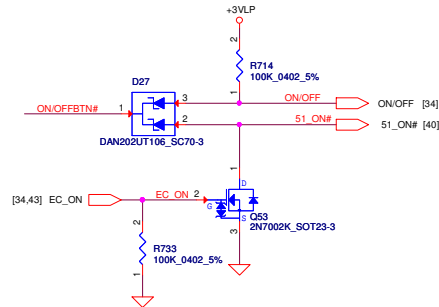
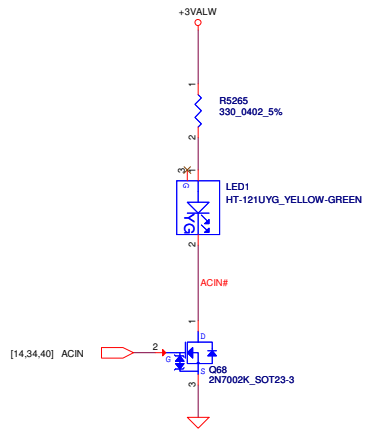


Remove pull-low

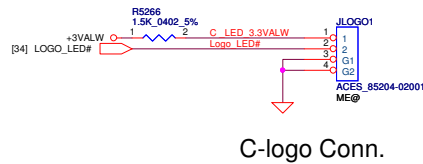
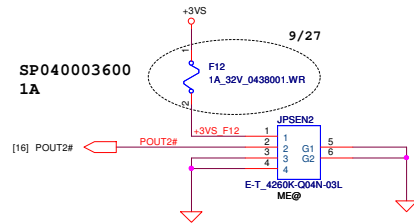
|                                   | A_PRE1/B_PRE1<br>(Internal<br>pull Low) | A_PRE0/B_PRE0<br>(Internal<br>pull Low) |
|-----------------------------------|---|---|
| 0dB, no pre-emphasis              | Low                                     | Low                                     |
| 1.5dB pre-emphasis<br>is selected | Low                                     | High                                    |
| 2.5dB pre-emphasis<br>is selected | High                                    | Low                                     |
| 3.5dB pre-emphasis<br>is selected | High                                    | High                                    |



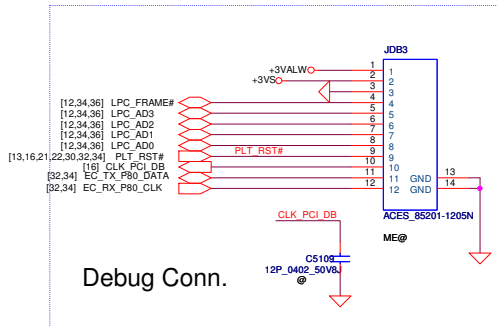
## AC-IN LED (Color : Green)



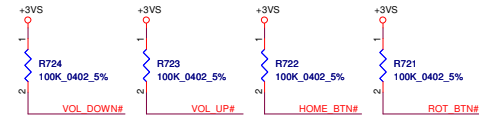
SP040003600  
1A



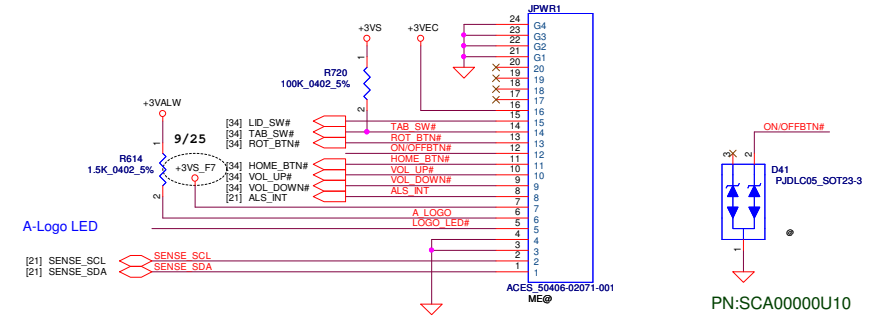
C-Logo Conn.



Debug Conn.

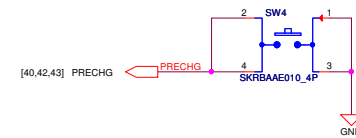


SP010016L00



Power Board connector

BOTTOM SIDE  
SN111005800 by ME drawing

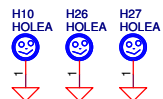


For Power Reset

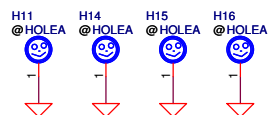
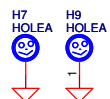
|   |                                  |                 |                         |                    |
|---|----------------------------------|-----------------|-------------------------|--------------------|
| Security Classification   | Compal Secret Data               |                 | Compal Electronics Ltd. |                    |
| Issued Date   | 2010/08/25                       | Deciphered Date | 2012/08/25              | Title              |
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| Size  | Document Number                  | LA-8671P_SDV    | Rev                     | 1A                 |
| Custom  | Date: Thursday, October 04, 2012 | Sheet           | 37                      | of 50              |

# PT-H 20 SCREW HOLE

H\_2P3



H\_3P3



H\_6P0

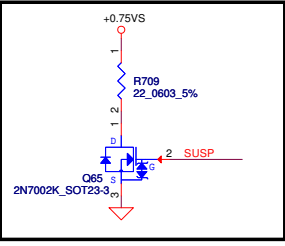
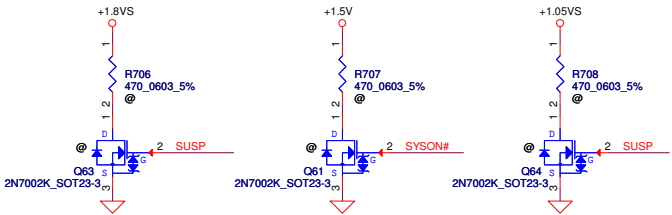
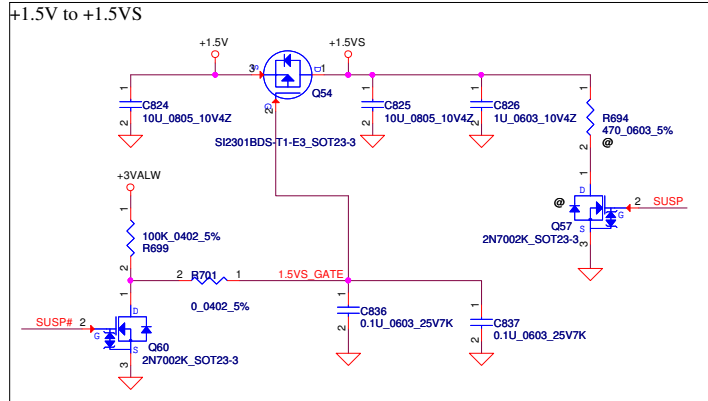
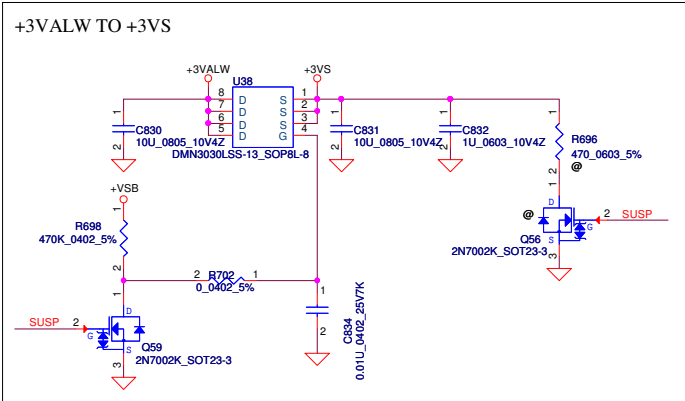
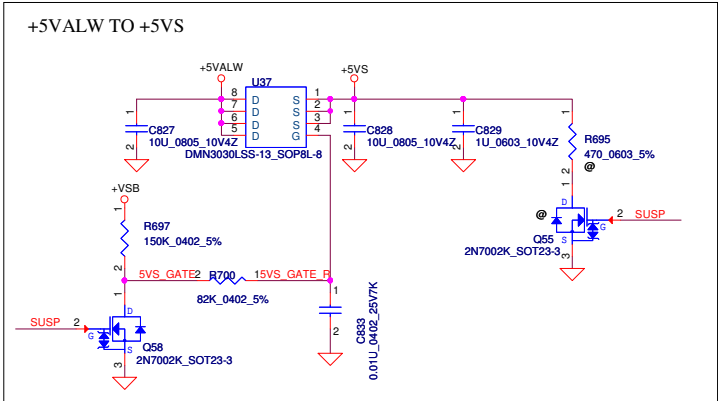


## NON-PT-H 1 SCREW HOLE

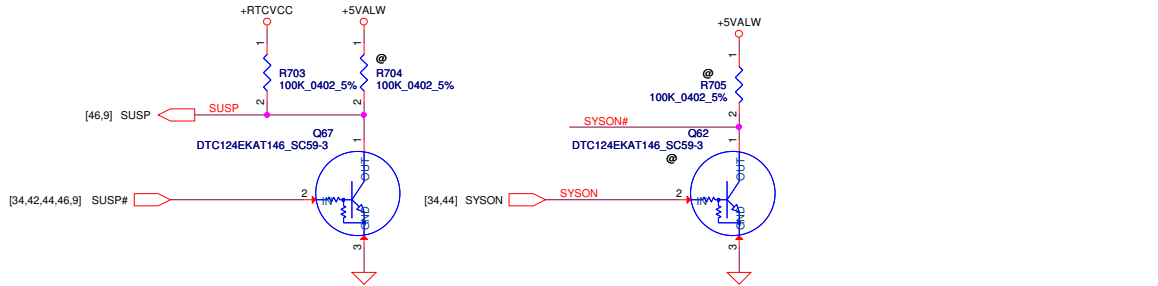
H\_2P4N



|   |                            |                 |                          |            |
|---|----------------------------|-----------------|--------------------------|------------|
| Security Classification   | Compal Secret Data         |                 | Compal Electronics, Inc. |            |
| Issued Date   | 2010/08/25                 | Deciphered Date | 2012/08/25               | Title      |
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| Size  | Document Number            | LA-8671P_SDV    |                          | Rev        |
| Custom  |                            |                 |                          | 1.0        |
| Date:   | Thursday, October 04, 2012 | Sheet           | 38                       | of 50      |



For Intel S3 Power Reduction.





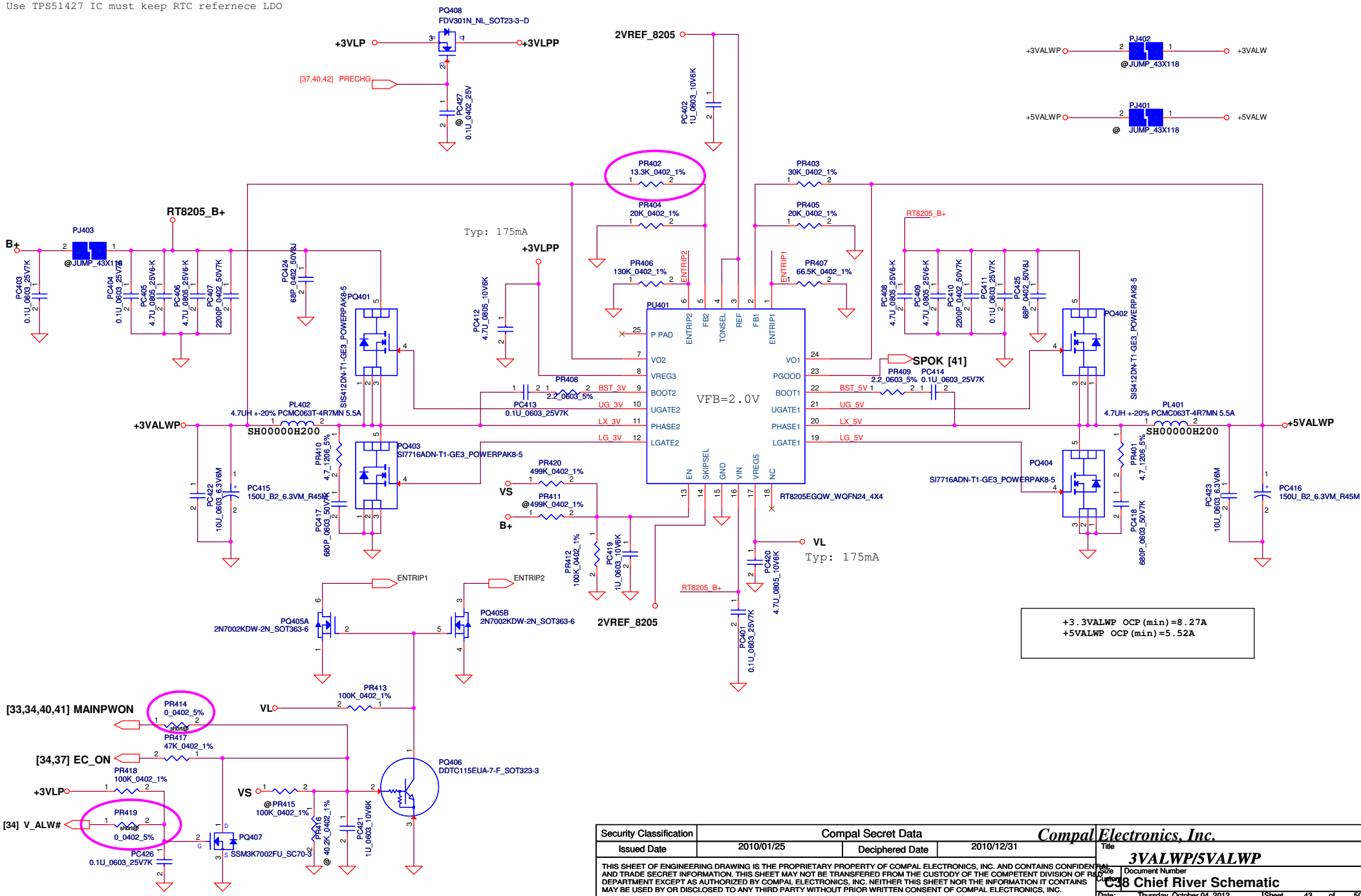




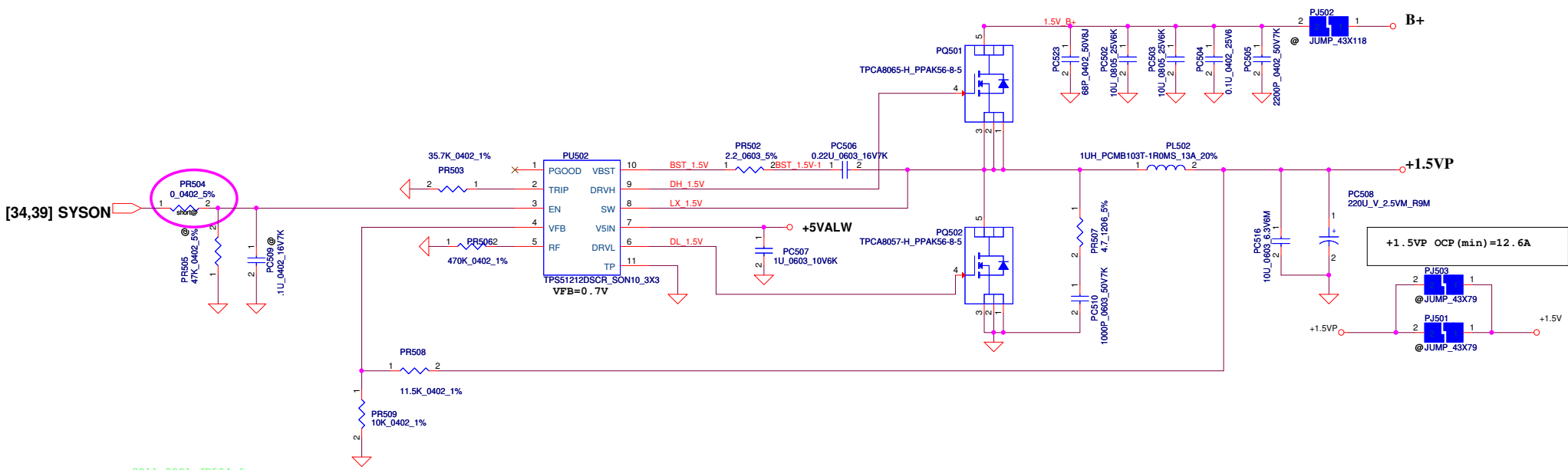


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| Security Classification   | Compal Secret Data |                 |            | Compal Electronics, Inc. |                            |     |
| Issued Date   | 2010/01/13         | Deciphered Date | 2011/01/13 | Title                    |                            |     |
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|   |                    |                 |            | Date                     | Thursday, October 16, 2008 | 1.0 |

Note:  
Use TPS51125 IC can remove RTC refernece LDO  
Use TPS51427 IC must keep RTC refernece LDO



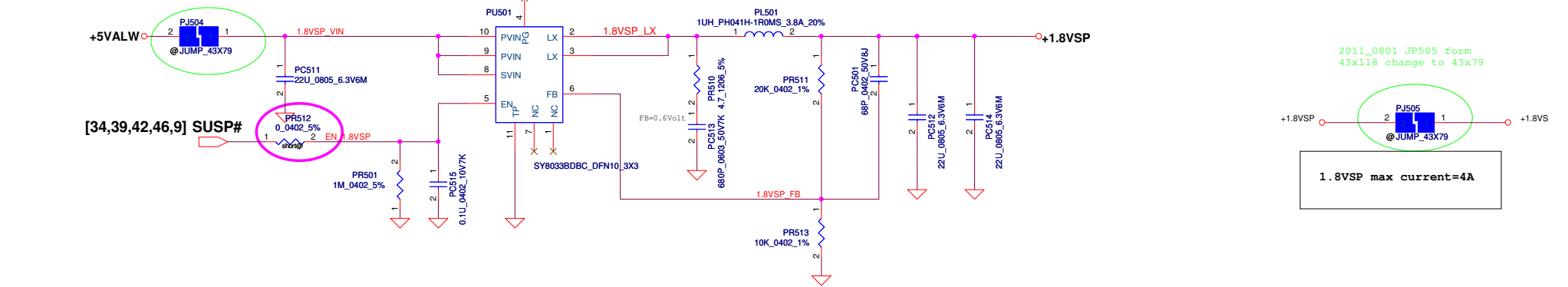
[34,39] SYSON



2011\_0801 JP504 form  
43x118 change to 43x79

+5VALW

[34,39,42,46,9] SUSP#



2011\_0801 JP505 form  
43x118 change to 43x79

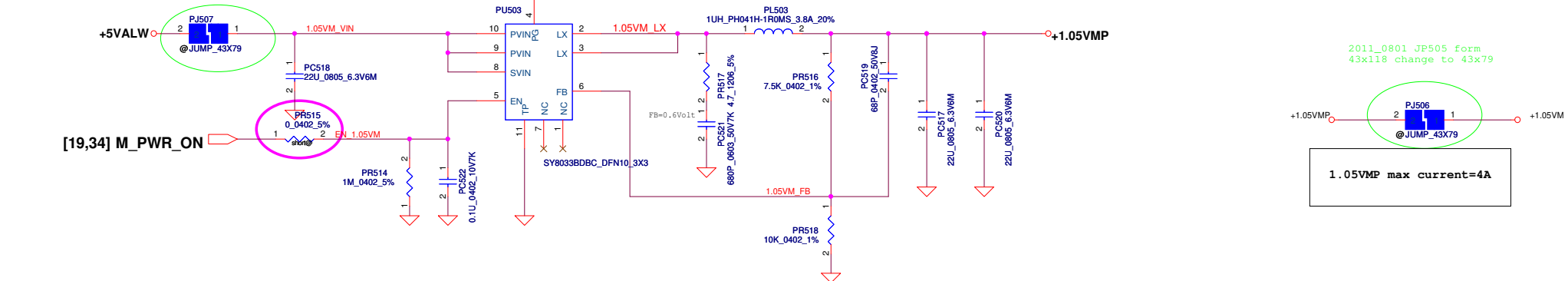
+1.8VSP

1.8VSP max current=4A

2011\_0801 JP504 form  
43x118 change to 43x79

+5VALW

[19,34] M\_PWR\_ON

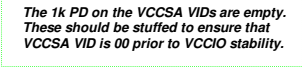


2011\_0801 JP505 form  
43x118 change to 43x79

+1.05VMP

1.05VMP max current=4A

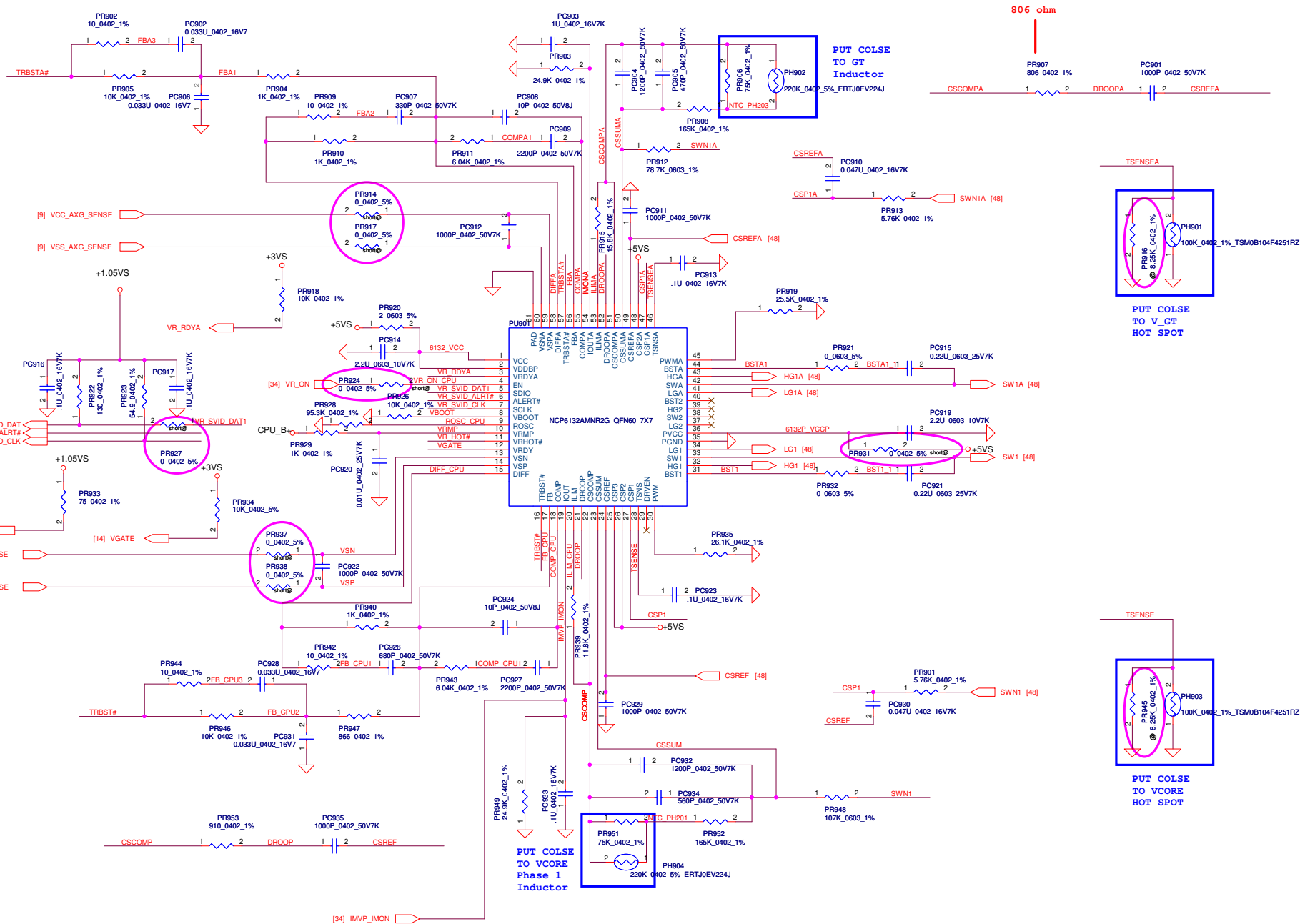
output voltage adjustable network



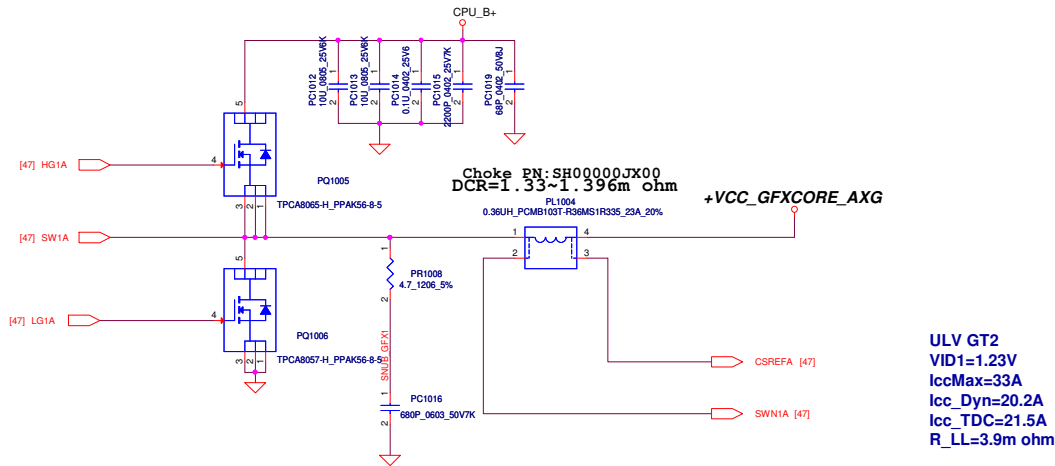
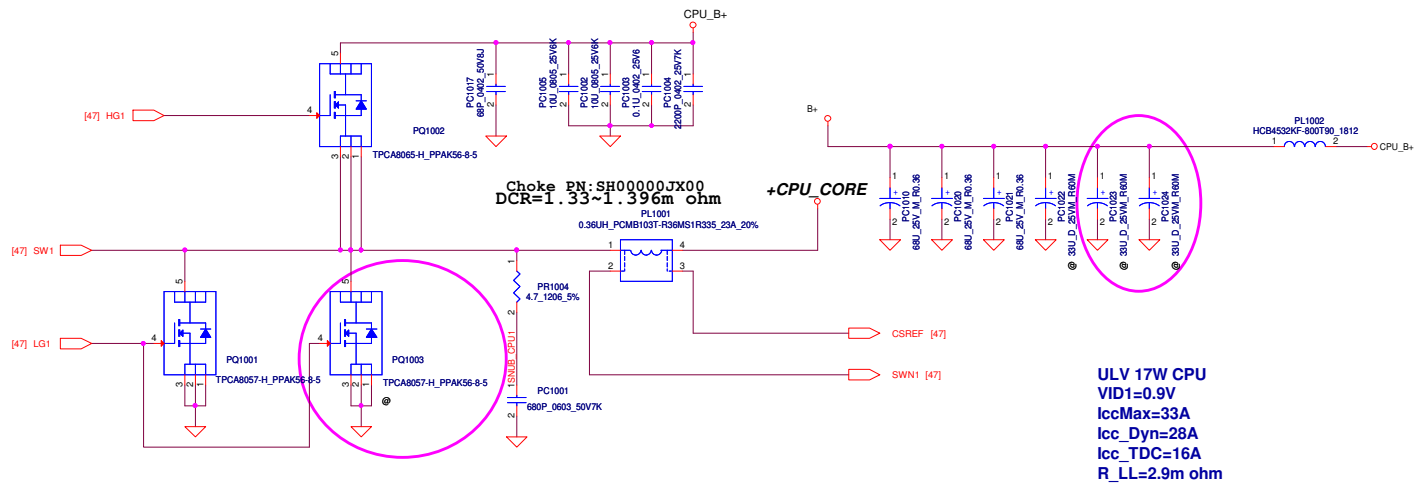
**WWW.AliSaler.Com**

|   |  |                    |  |  |  |
|---|--|--------------------|--|--|--|
| Security Classification   |  | Compal Secret Data |  | <i>Compal Electronics, Inc.</i><br><b>PWR + VCCSAP</b>   |  |
| Issued Date   |  | Deciphered Date    |  | Title<br><b>PWR + VCCSAP</b>                             |  |
| 2010/01/25  |  | 2010/12/31         |  | Size Document Number<br><b>C38 Chief River Schematic</b> |  |
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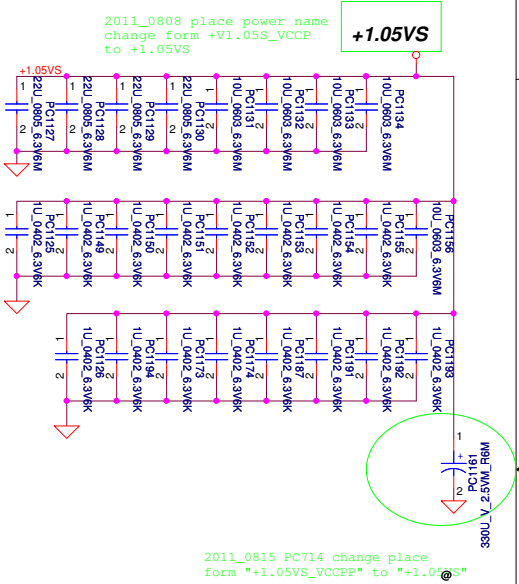
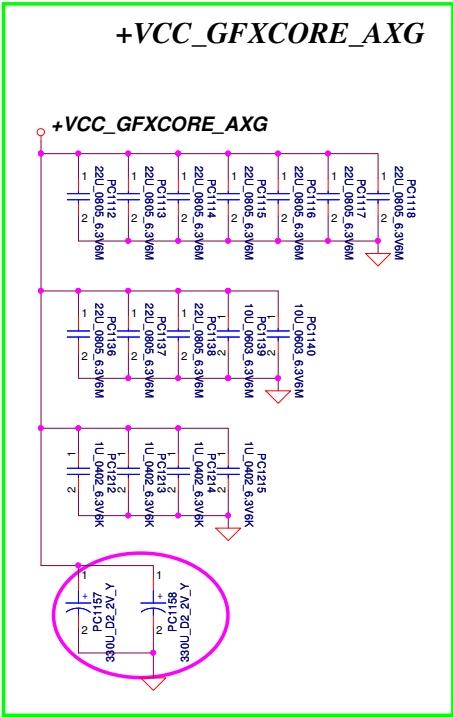
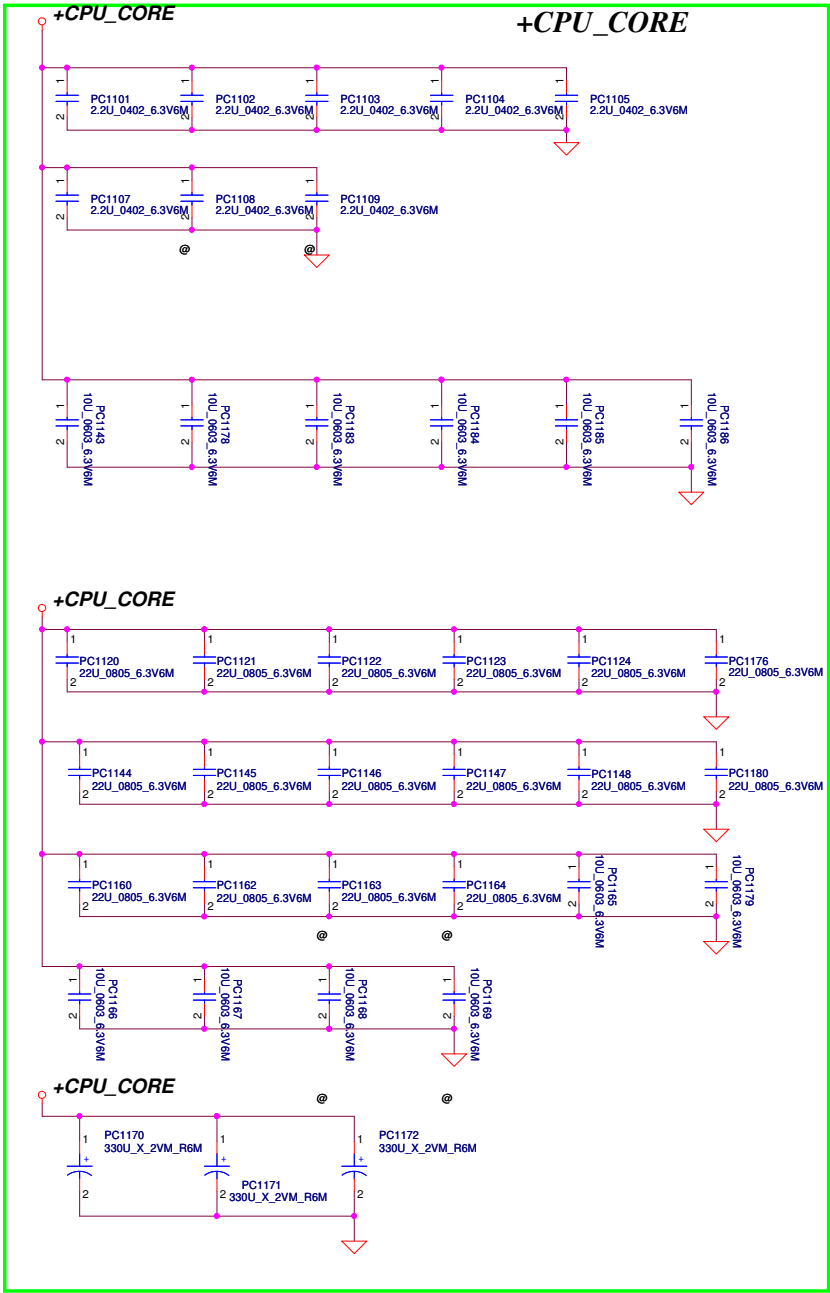


|   |            |                    |            |                          |                            |
|---|------------|--------------------|------------|--------------------------|----------------------------|
| Security Classification   |            | Compal Secret Data |            | Compal Electronics, Inc. |                            |
| Issued Date   | 2009/12/01 | Deciphered Date    | 2010/12/31 | Title                    | PWR-CPU_CORE               |
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| Size  | Custom                     | Document Number | C38 Chief River Schematic | Rev 1.0             |
| Date:   | Thursday, October 04, 2012 | Sheet           | 48                        | of 50               |





| Item | Reason for change                   | PG# | Modify List                                      | Date       | Phase |
|------|-------------------------------------|-----|--|------------|-------|
| 1    | NCP6132A update SPEC                | P47 | Remove PR916, PR945                              | 2012/06/05 |       |
| 2    | Thermal verify PH1 setting point    | P41 | Change PR208 value to 18.2K                      | 2012/06/08 |       |
| 3    | ME demand : Keyboard assembly issue | P44 | Change PL401, PL402 to High=2.4mm (SH000000H200) | 2012/06/08 |       |
| 4    | Noise is PASS, Don't need 33u CAP   | P48 | Remove PC1023, PC1024                            | 2012/06/08 |       |
| 5    |                                     |     |  |            |       |
| 6    |                                     |     |  |            |       |
| 7    |                                     |     |  |            |       |
| 8    |                                     |     |  |            |       |
| 9    |                                     |     |  |            |       |
| 10   |                                     |     |  |            |       |
| 11   |                                     |     |  |            |       |
| 12   |                                     |     |  |            |       |
| 13   |                                     |     |  |            |       |
| 14   |                                     |     |  |            |       |
| 15   |                                     |     |  |            |       |
| 16   |                                     |     |  |            |       |
| 17   |                                     |     |  |            |       |

|   |            |                    |            |                           |          |
|---|------------|--------------------|------------|---------------------------|----------|
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| Issued Date   | 2009/01/06 | Deciphered Date    | 2009/01/06 | Title                     |          |
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|   |            |                    |            | C38 Chief River Schematic |          |
| Date: Thursday, October 04, 2012  |            |                    |            | Sheet                     | 50 of 50 |
|   |            |                    |            | Rev                       | 1.0      |